

# COMPAL CONFIDENTIAL

MODEL NAME :  
FDI40 N3-14", FDI4A V3-14", FDI43 N5-14",  
FDI50 N3-15", FDI5A V3-15", FDI53 N5-15",  
FDI70 N3-17"

PCB NO : DA8001K6000 (SBDR)  
DA8001K6100 (NBDR)

BOM P/N :  
SBDR-DIS NBDR-DIS X4E  
431AJ431L51 431AJ431L01 X4E AJ431L51(N3)  
431AJ431L52 431AJ431L02 X4E AJ431L52(V3)  
431AJ431L53 X4E AJ431L01(N5)  
431AJ431L54

## CML-U+MEC1418

### 2019-06-20

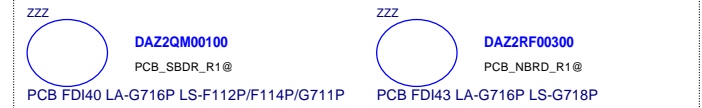
### REV : 1.0 (A00)

@ : Un-pop Component  
DIS@ : GPU Support  
INSPIRON@/VOSTRO@ : Inspiron/Vostro  
CML@/CNL@ : CML/CNL  
BASE@/PREM@ : Pentium,Celeron / i3,i5,i7  
EXO@/TOPAZ@ : R17M-M1-30/R17M-M2-50  
EC@ : EC Support  
JP@/PJP@ : JUMP  
100@/1000@: Lan

EMI@/ESD@/RF@ : EMI, ESD and RF Component  
@EMI@/@ESD@/@RF@ : EMI, ESD and RF Un-POP Component  
CMC@ : XDP Component  
CONN@ : Connector Component  
KBBL@ : KB Backlight  
TPM@/FTPM@ : HW TPM/SW TPM  
750\_CTPM@:750 and china TPM  
ST\_CTPM@:ST and china TPM  
CTPM@:China TPM  
FFS@ : Free Fall Sensor  
TYPEC@ : TypeC  
TYPEC@EMI@/TYPEC@ESD@: EMI, ESD ,TypeC Component  
M1\_30@/M2\_50@ : R17M-M1-30/R17M-M2-50  
2G\_G5@/2G\_H@/2G\_S@/2G\_M@ : VRAM type  
PCB@/PCB\_R1@/PCB\_R3\_G@/PCB\_R3\_T@/PCB\_R3\_H@/PCB\_R3\_TM@: PCB MB

PCB\_NBDR\_R1@: PCB MB DAZ for NBDR  
TS\_NON@/TS\_USB@/TS\_I2C@: Touch Screen Interface Select  
SBDR@/NBDR@: SBDR/ NBDR Select

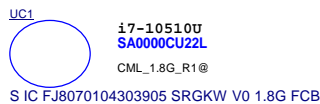
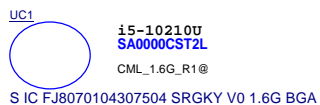
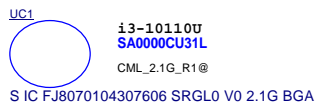
### PCB R1



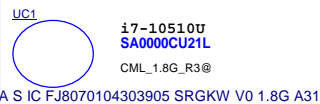
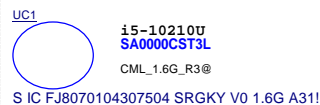
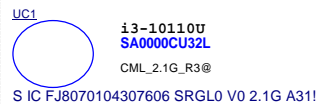
### PCB R3



### CPU R1



### CPU R3



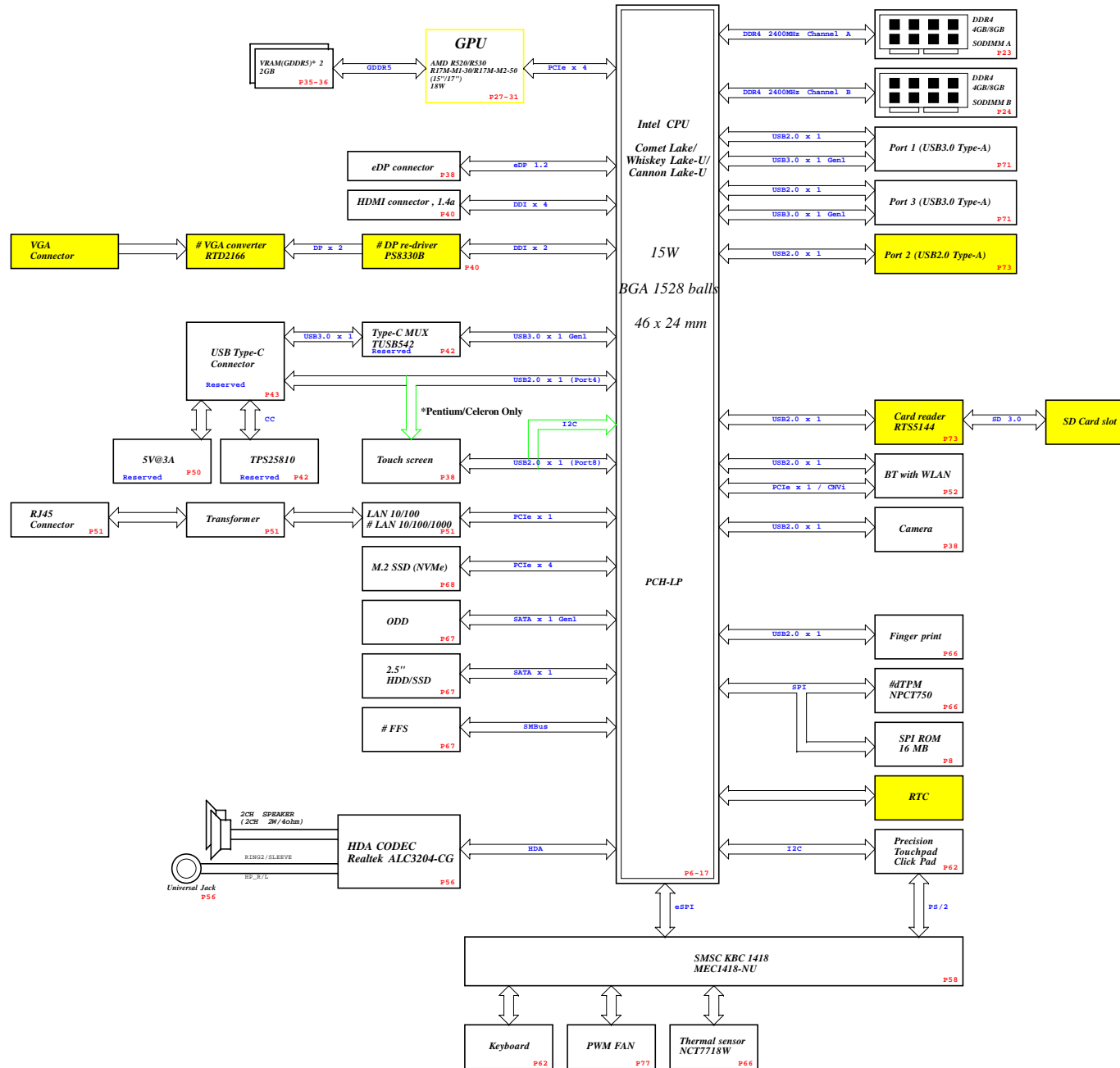
Layout Dell logo



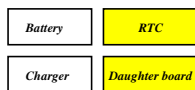
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REV: X01  
PWB: 9HTP8

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				Size	Document Number
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				Date:	Thursday, June 20, 2019
				Sheet	1 of 101
				Rev	1.0

# Block Diagram



# ----> V3 only



Security Classification	Control Secret Data	Declassified Date	20250630
Issued Date	20190608	Declassified Date	20250630
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Doc#	LA-C716P	Rev	1.0
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POWER STATES

Signal	SLP S3#	SLP S4#	SLP S5#	ALWAYS PLANE	SUS PLANE	RUN PLANE	CLOCKS
State							
S0 (Full ON) / M0	HIGH	HIGH	HIGH	ON	ON	ON	ON
S3 (Suspend to RAM) / M3	LOW	HIGH	HIGH	ON	ON	OFF	OFF
S4 (Suspend to DISK) / M3	LOW	LOW	HIGH	ON	OFF	OFF	OFF
S5 (SOFT OFF) / M3	LOW	LOW	LOW	ON	OFF	OFF	OFF
G3	OFF	OFF	OFF	OFF	OFF	OFF	OFF
DS3	---	---	---	---	---	---	---

USB 2.0	DESTINATION
1	USB2.0 port1
2	USB2.0 Port2
3	USB2.0 Port, IO/B
4*	Touch screen
5	Finger printer
6	Camera
7	Card reader , IO/B
8*	Reserved
9	NA
10	BT

Voltage Rails

Power Plane	Description	S0	S3	S4/S5
+19V_ADPIN	Adapter power supply	N/A	N/A	N/A
+17.4V_BATT++	Battery power supply	N/A	N/A	N/A
+19VB	AC or battery power rail for System	N/A	N/A	N/A
+RTC_CELL	RTC power	ON	ON	ON
+3VALW_DSW	+3VALW power for PCH DSW rails	ON	ON	ON*
+5VALW	System +5V always on power rail	ON	ON	ON*
+3VALW	System +3V always on power rail	ON	ON	ON*
+1.8V_PRIM	System +1.8V always on power rail	ON	ON	ON*
+1.0V_PRIM	System +1.0V always on power rail	ON	ON	ON*
+1.2V_DDR	DDR4 +1.2V power rail	ON	ON	OFF
+2.5V_MEM	DDR4 +2.5V power rail	ON	ON	OFF
+0.6V_DDR_VTT	DDR +0.6V power rail for DDR terminator	ON	OFF	OFF
+VCCST	+1.05 VCCST power rail	ON	ON	OFF
+VCCSTG	+1.05 VCCSTG power rail	ON	OFF	OFF
+VCCIO	+1.05 VCCIO power rail	ON	OFF	OFF
+VCC_CORE	Core voltage for CPU	ON	OFF	OFF
+VCC_GT	Sliced graphics power rail	ON	OFF	OFF
+VCC_SA	System Agent power rail	ON	OFF	OFF
+3VLP	+19VB to +3VLP power rail for suspend power	ON	ON	ON
+3VALW_PCH	+3VALW power for PCH suspend rails	ON	ON	ON*
+5VS	System +5VS power rail	ON	OFF	OFF
+3VS	System +3VS power rail	ON	OFF	OFF
+1.35V_MEM GFX	+1.35V power rail for GPU	ON	OFF	OFF
+3VGS	+3V power rail for GPU	ON	OFF	OFF
+1.8VGS	+1.8V power rail for GPU	ON	OFF	OFF
+0.95VSDGPU	+0.95V power rail for GPU	ON	OFF	OFF

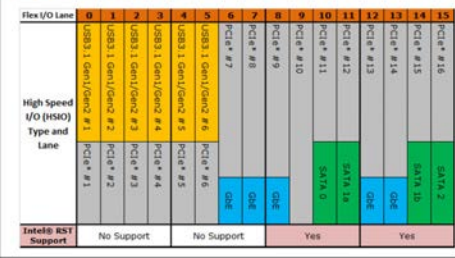
Note : ON\* means that this power plane is ON only with AC power available, otherwise it is OFF

Board ID & Model ID Table

#	Pull-down	Pull-up	Voltage	Board ID	Model ID
1	100	10.0	3.000	EVT	SBDR-UMA
2	100	17.8	2.801	DVT1	
3	100	27.0	2.598		NBDR-UMA
4	100	37.4	2.402	DVT2	
5	100	49.9	2.201		SBDR-DSC
6	100	64.9	2.001	Pilot	
7	100	82.5	1.808		NBDR-DSC
8	100	107.0	1.594		
9	100	154.0	1.299		
10	100	200.0	1.100		

USB 3.0	PCIE	SATA	DESTINATION
USB3.0-1	PCIE-1		USB3.0 port1
USB3.0-2	PCIE-2		NA
USB3.0-3	PCIE-3		USB3.0 port2
USB3.0-4	PCIE-4		TypeC
USB3.0-5	PCIE-5		GPU
USB3.0-6	PCIE-6		GPU
	PCIE-7		GPU
	PCIE-8		GPU
	PCIE-9		LOM
	PCIE-10		WLAN
	PCIE-11	SATA-0	SATA HDD
	PCIE-12	SATA-1A	SATA ODD
	PCIE-13		NVME SSD
	PCIE-14		NVME SSD
	PCIE-15	SATA-1B	NVME SSD
	PCIE-16	SATA-2	NVME SSD

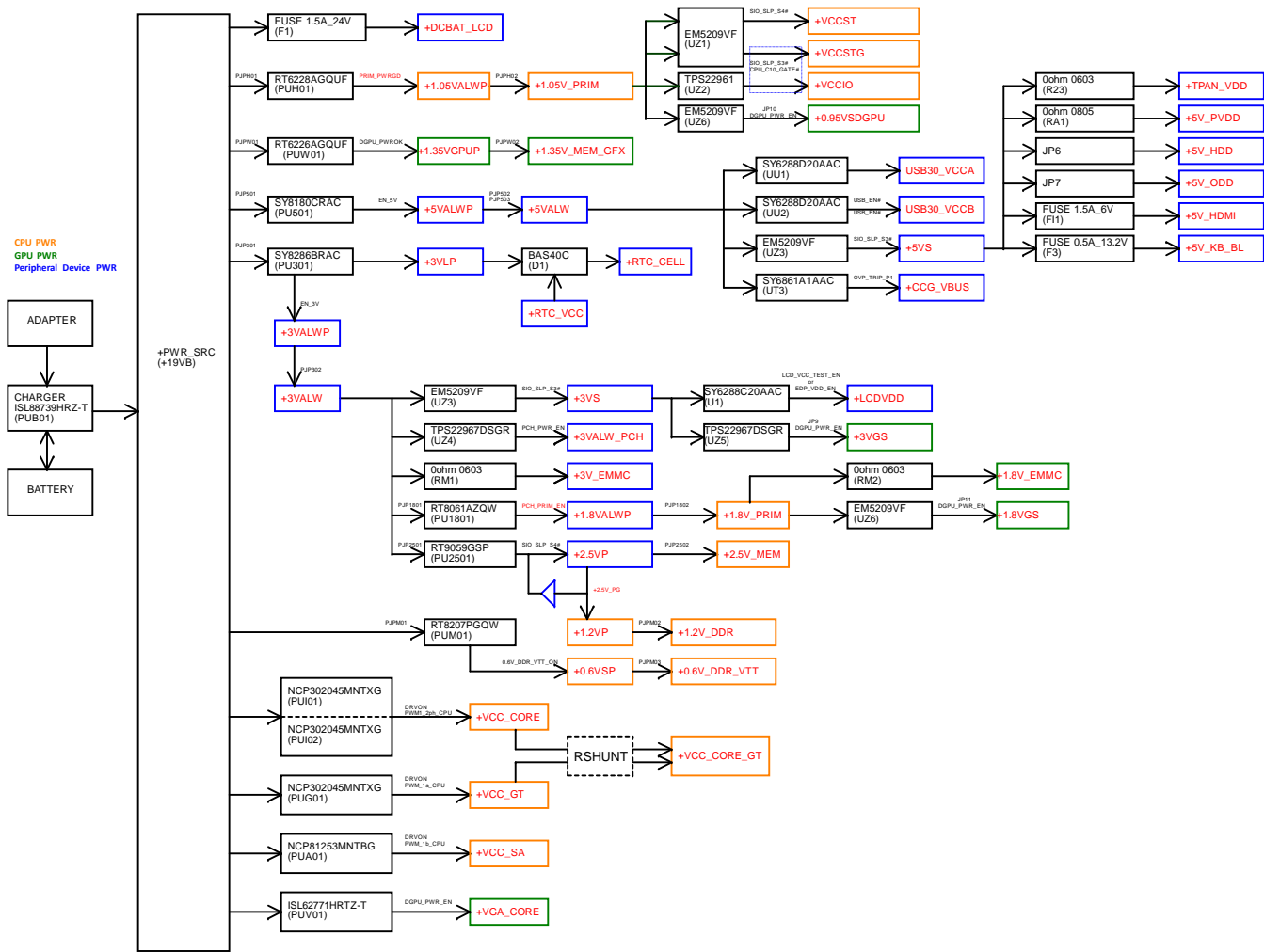
Figure 6-1. High Speed I/O (HSIO) Lane Multiplexing in CNL PCH-LP



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Rev	Document	Version	Rev	1.0
LA-G716P				
Date	Thursday, June 20, 2019	Revised	3	of 181

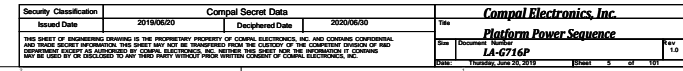
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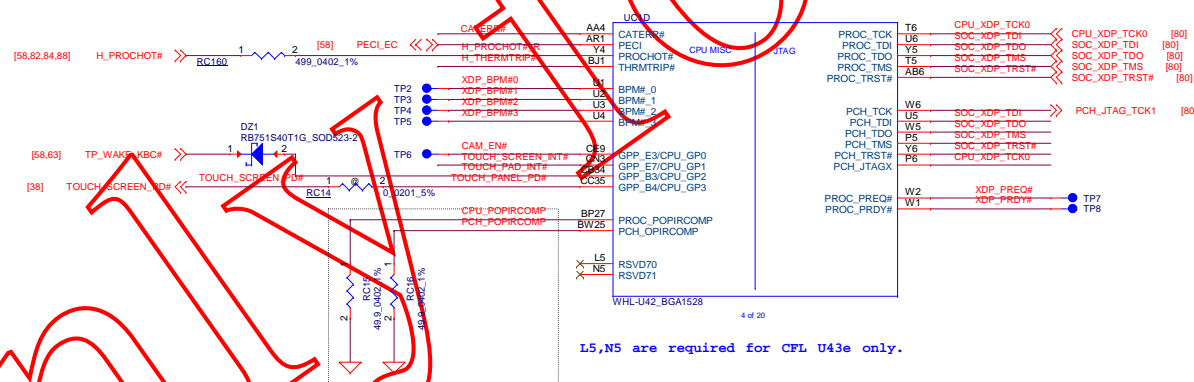
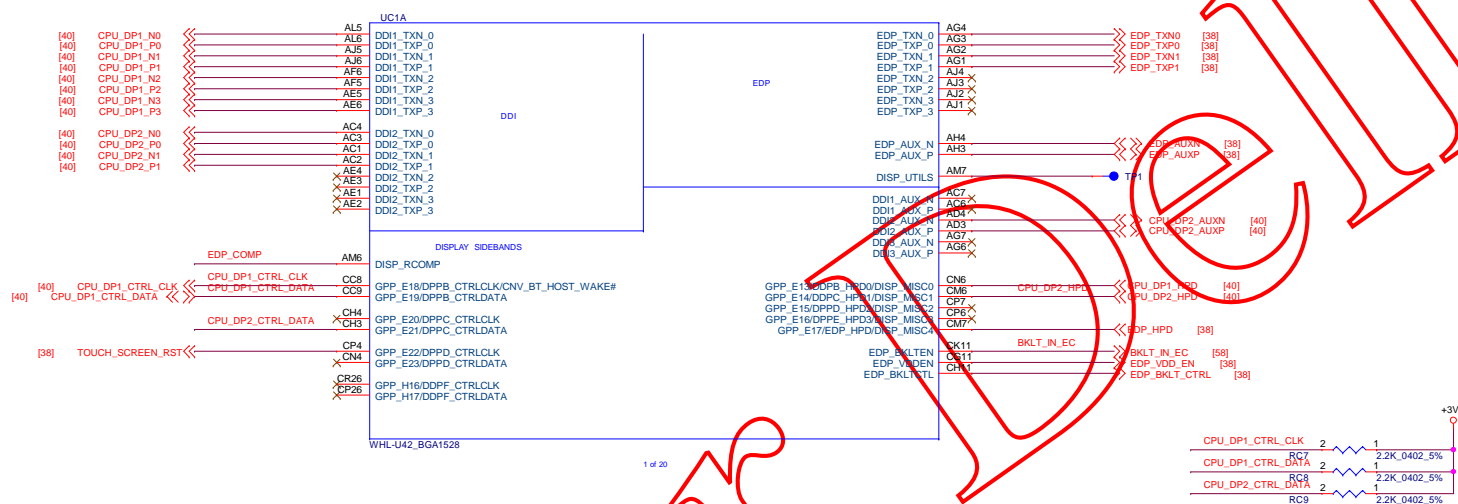
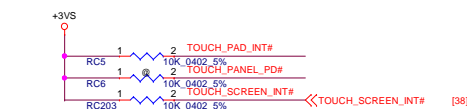
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Title				Power MAP
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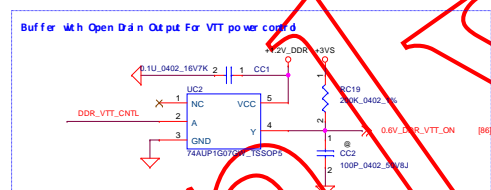
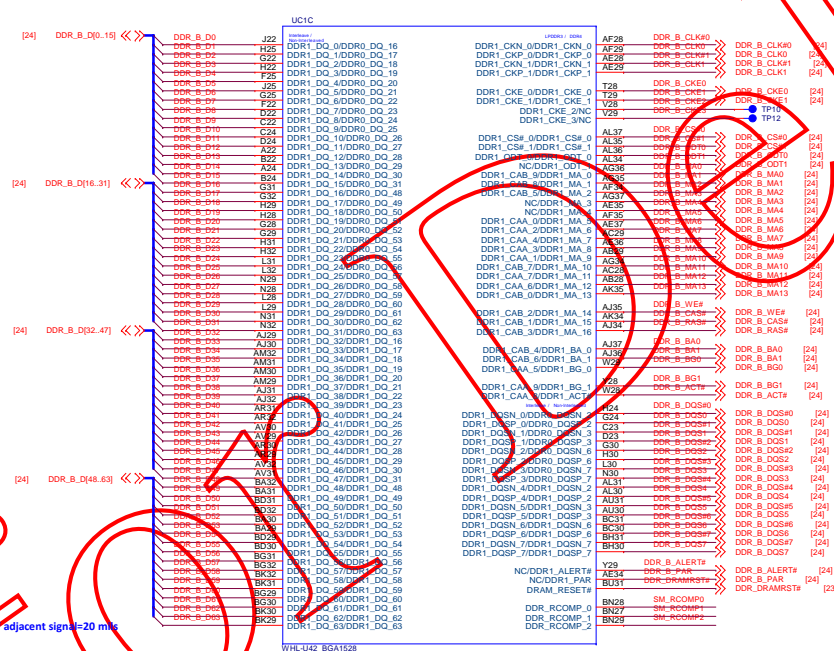
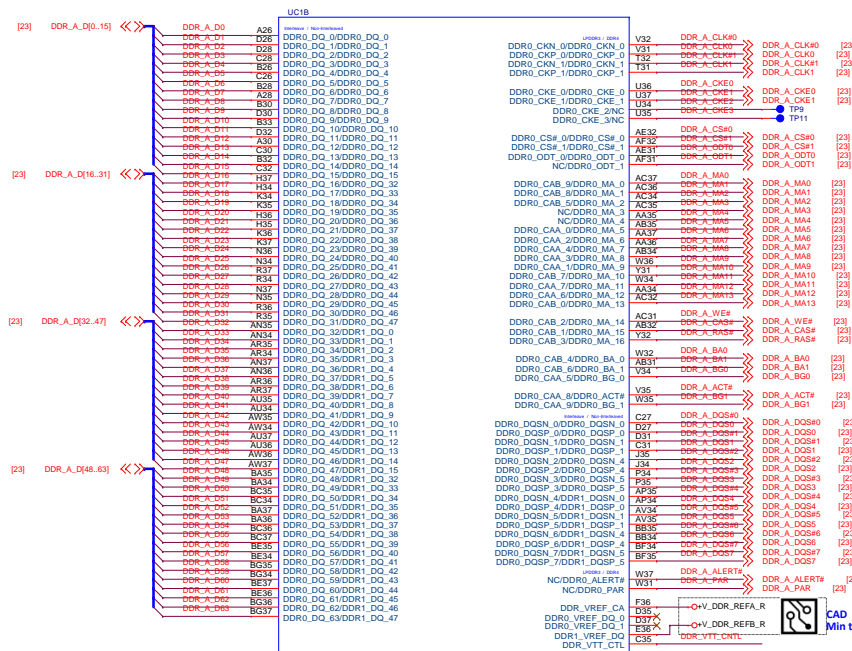




**COMPENSATION FOR OPIRCOMP**  
CAD Note:  
Min trace width=10 mils, Max trace length=500 mils

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Issued Date	2019/06/20	Deciphered Date	2020/06/30	Title	CPU(1/12)DDL,MSIC,XDP,EDP	
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				Date:	Thursday, June 20, 2019	Sheet

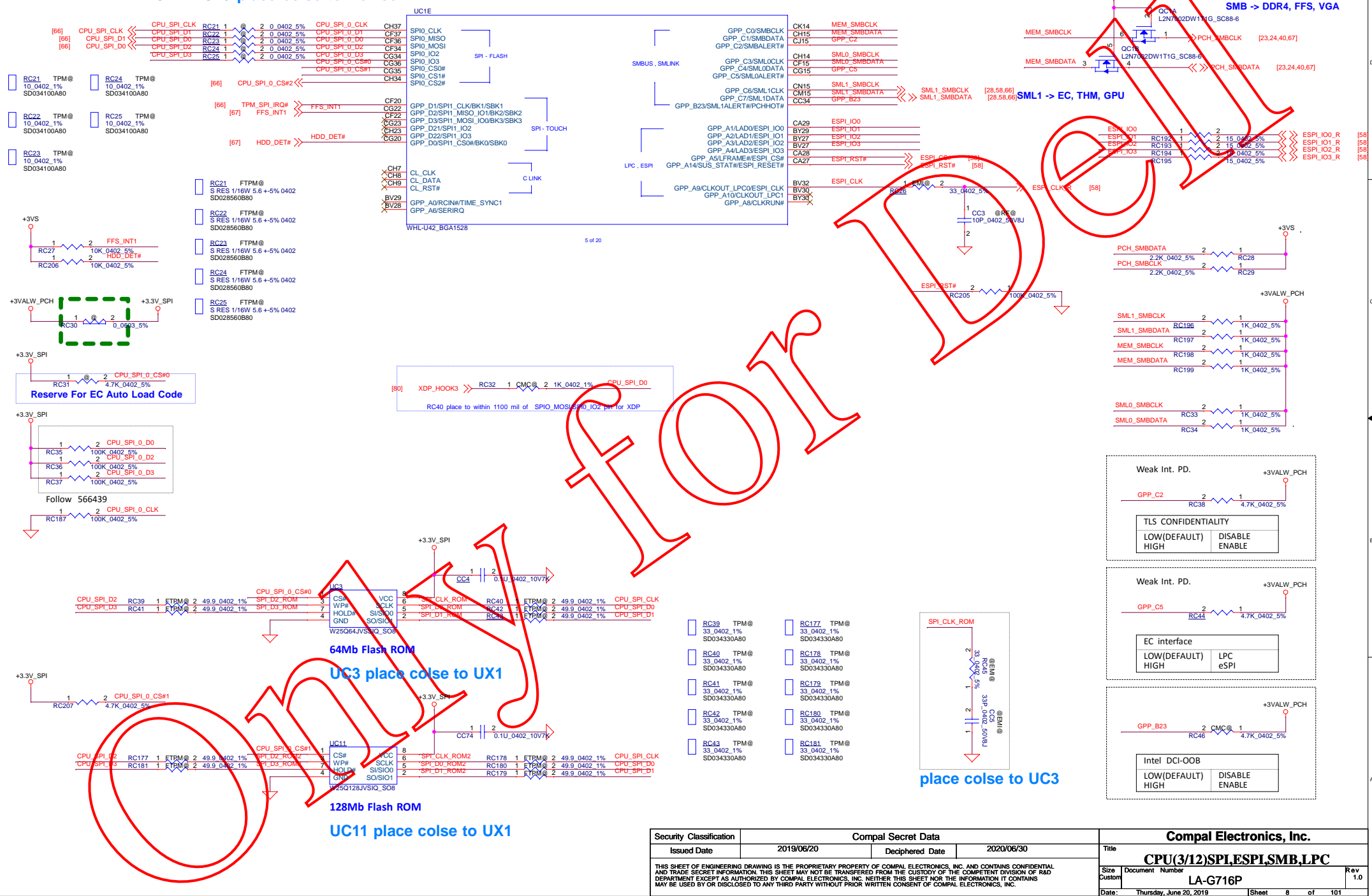
## DDR4 Interleaved Memory



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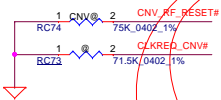
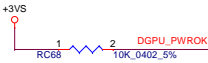
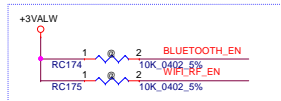
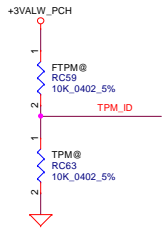
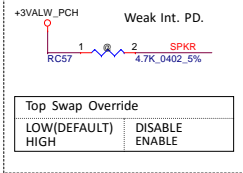
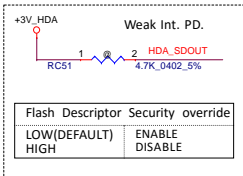


**RC21~RC25 place colse to Device**





ME\_FWP  
LOW = ENABLE -->ME lock, can't update ME  
HIGH = DISABLE -->ME un-lock, can update ME



**COMPENSATION FOR CNV\_WT\_RCOMP**

CAD Note:  
Min trace width=10 mils, Spacing=15 mils  
Max trace length=500 mils

CPU ID	BOARD_ID2 (GPP_C11)	BOARD_ID1 (GPP_C12)
CML-U	---	RC70
WHL-U(Reserved)	---	RC72
Pentium/Celeron	RC69	---
i3/i5/i7	RC71	---

**COMPENSATION FOR SD\_RCOMP**

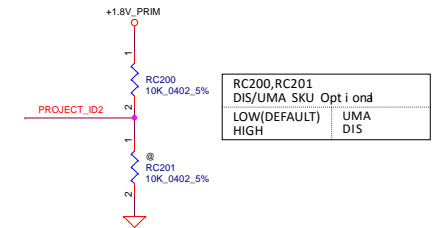
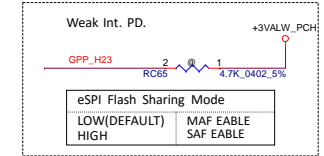
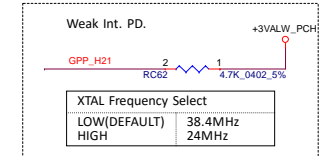
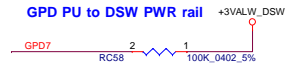
CAD Note:  
Min trace width=10 mils, Spacing=12 mils  
Max trace length=500 mils

**COMPENSATION FOR SD\_RCOMP**

CAD Note:  
Min trace width=10 mils, Spacing=12 mils  
Max trace length=500 mils

**COMPENSATION FOR EMMC\_RCOMP**

CAD Note:  
Min trace width=10 mils, Spacing=12 mils  
Max trace length=500 mils



CPU ID	BOARD_ID2 (GPP_C11)	BOARD_ID1 (GPP_C12)
CML-U	---	RC70
WHL-U(Reserved)	---	RC72
Pentium/Celeron	RC69	---
i3/i5/i7	RC71	---

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		CPU(4/12)HDA,EMMC,SD
Size	Document Number	Rev
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RCR7, RCR8, RCR9, RCR10 close to Device

UC1J

CLOCK SIGNALS

GPU----> [27] CLK\_PEG\_N0  
[28] CLK\_PEG\_P0  
CLKREQ\_PEG#0

WLAN----> [52] CLK\_PCIE\_N1  
[52] CLK\_PCIE\_P1  
CLKREQ\_PCIE#1

LAN----> [51] CLK\_PCIE\_N2  
[51] CLK\_PCIE\_P2  
CLKREQ\_PCIE#2

SSD----> [68] CLK\_PCIE\_N4  
[68] CLK\_PCIE\_P4  
CLKREQ\_PCIE#4

RCR7 1 10K 0402 5%

RCR8 1 10K 0402 5%

RCR9 1 10K 0402 5%

RCR10 1 10K 0402 5%

AW2  
AY3  
CF32  
BC1  
BC2  
CE32  
BD3  
BC3  
CF30  
BH3  
BH4  
CE31  
BA1  
BA2  
CE30  
BE1  
BE2  
CF31

CLKOUT\_PCIE\_N\_0  
CLKOUT\_PCIE\_P\_0  
GPP\_B8/SRCLCKREQ#0  
CLKOUT\_PCIE\_P\_1  
GPP\_B6/SRCLCKREQ#1  
CLKOUT\_PCIE\_N\_2  
CLKOUT\_PCIE\_P\_2  
GPP\_B7/SRCLCKREQ#2  
CLKOUT\_PCIE\_N\_3  
CLKOUT\_PCIE\_P\_3  
GPP\_B8/SRCLCKREQ#3  
CLKOUT\_PCIE\_N\_4  
CLKOUT\_PCIE\_P\_4  
GPP\_B9/SRCLCKREQ#4  
CLKOUT\_PCIE\_N\_5  
CLKOUT\_PCIE\_P\_5  
GPP\_B10/SRCLCKREQ#5

GPDB/SUSCLK  
XTAL\_IN  
XTAL\_OUT  
XCLK\_BIASREF  
CLINK\_XTAL  
RTCK1  
RTCK2  
SRTCSTW#  
RTCKSTW#

WHL-U42\_BGA1528

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**COMPENSATION FOR XCLK\_BIASREF**  
CAD Note:  
Min trace width=10 mils, Spacing=15 mils  
Max trace length=1000 mils

U4 change CPU #  
Description only  
Jason 2019-04-26

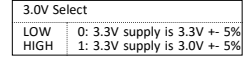
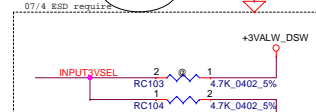
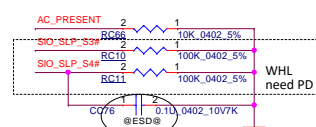
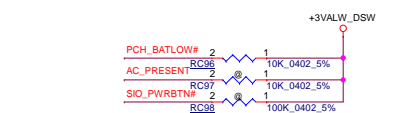
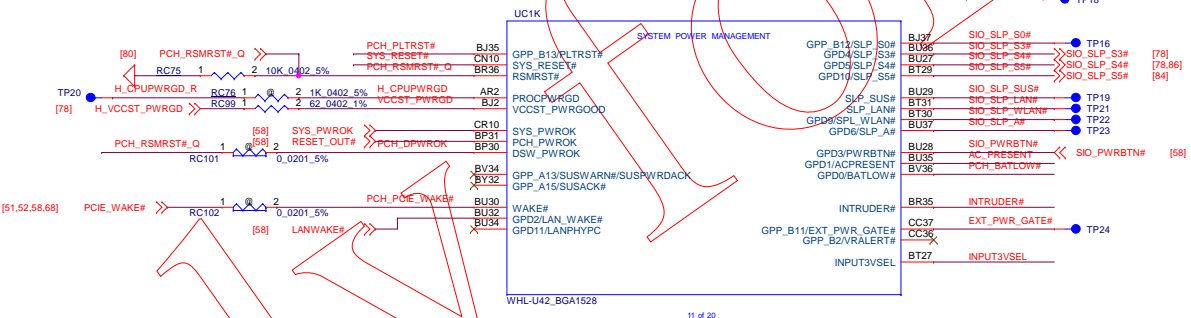
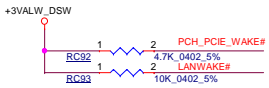
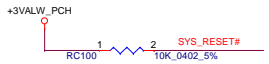
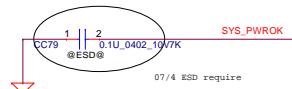
PCH\_PLTRST#

UC4

MC74VHC1G08EDFT2G\_SC70

PLTRST# [27,51,52,58,66,68]

ESD07/4 ESD require



RSMRST circuit

10k

10k

+3V3

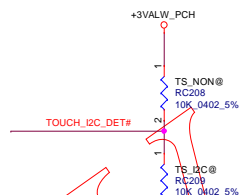
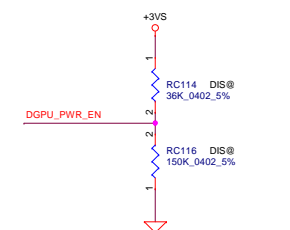
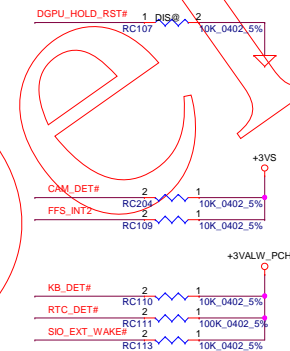
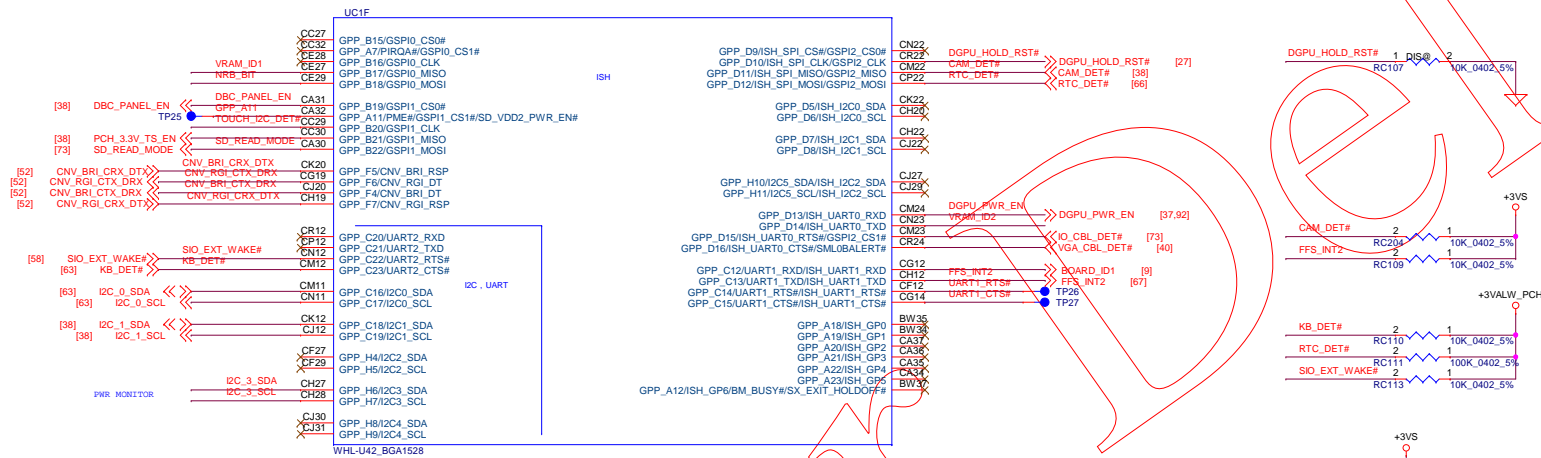
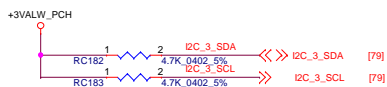
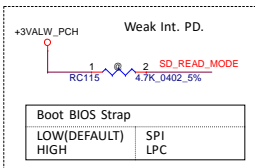
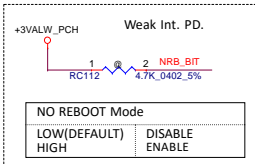
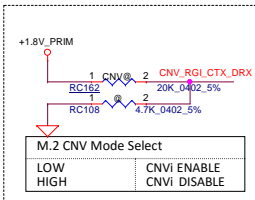
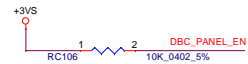
UC5 MC74VHC1G08EDFT2G SC70

UC5 Change CFW 4 Description only Jason 2019-04-26

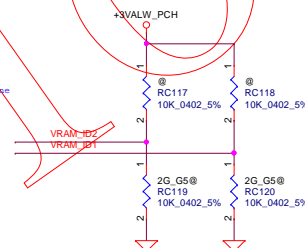
[58] PCH\_RSMRST#

PCH\_RSMRST#\_Q

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RC208,RC209	
Touch panel interface	
LOW	I2C
HIGH	USB



VRAM ID (PCBA VRAM Size Config.)	VBIOS_ID2 (GPP_CN23)	VBIOS_ID1 (GPP_CE27)
2G GDDR5	0	0
Reserved	0	1
Reserved	1	0
Reserved	1	1

**\*Combine in X76**

[27] PEG\_CRX\_GTX\_N[5..8] >>> PEG\_CRX\_GTX\_N[5..8]  
[27] PEG\_CRX\_GTX\_P[5..8] >>> PEG\_CRX\_GTX\_P[5..8]  
[27] PEG\_CTX\_GRX\_N[5..8] <<< PEG\_CTX\_GRX\_N[5..8]  
[27] PEG\_CTX\_GRX\_P[5..8] <<< PEG\_CTX\_GRX\_P[5..8]

GPU ---->

LOM ---->

WLAN ---->

SATA HDD ---->

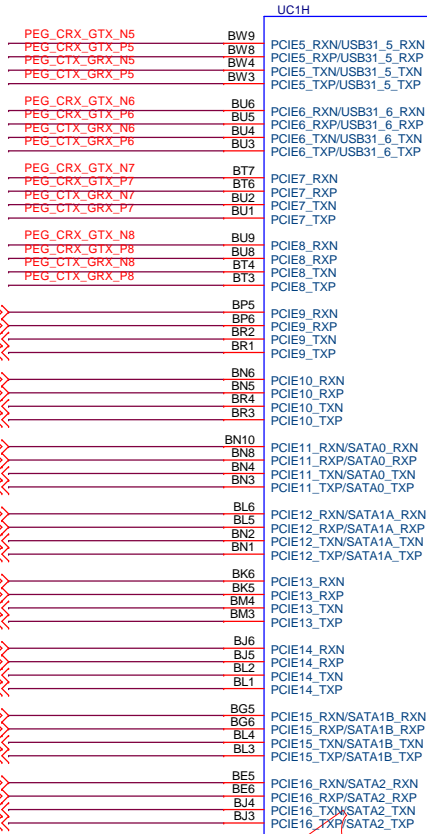
SATA ODD ---->

PCIE SSD ---->



#### COMPENSATION FOR PCIE\_RCOMP

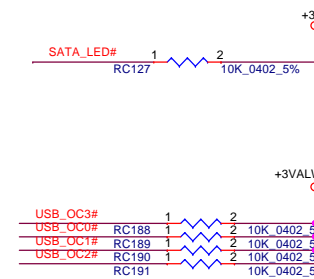
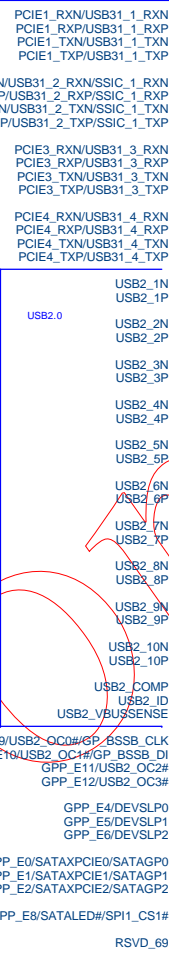
CAD Note:  
Min trace width=10 mils  
Max trace mismatch=5 mils



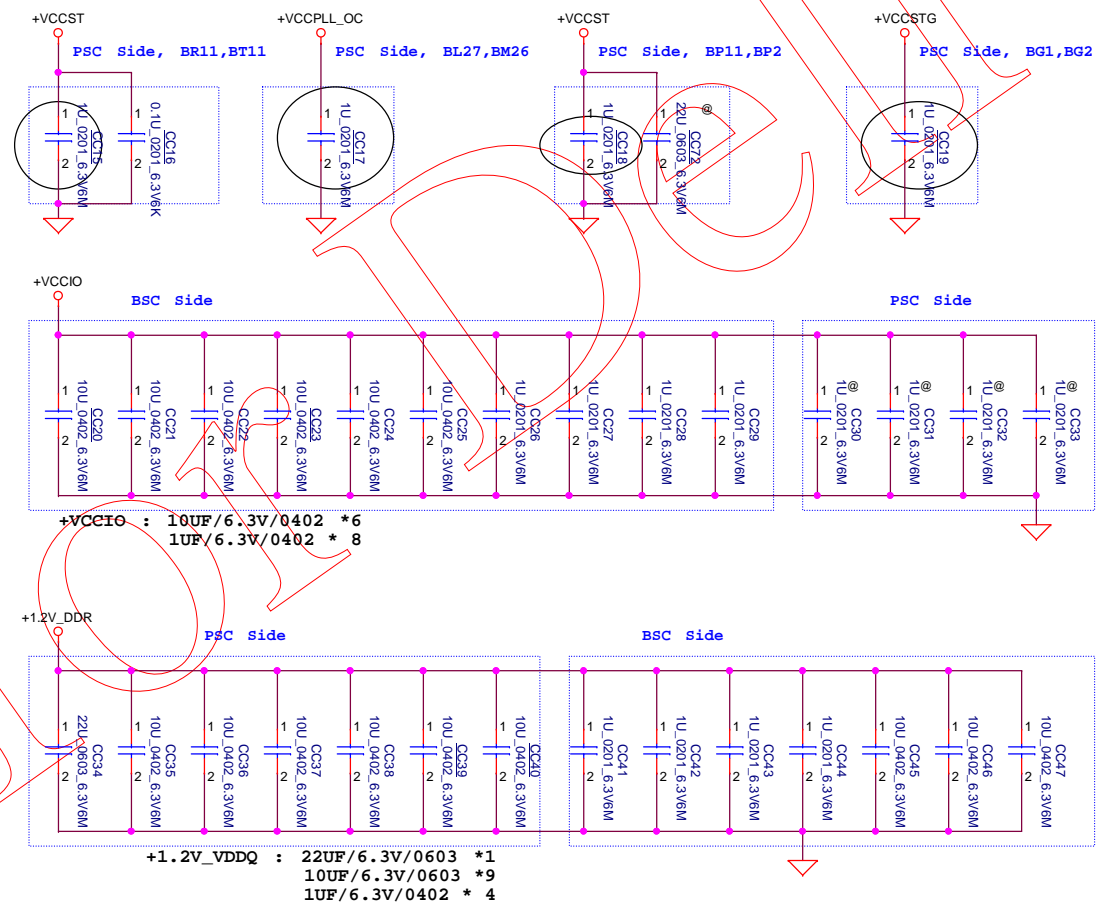
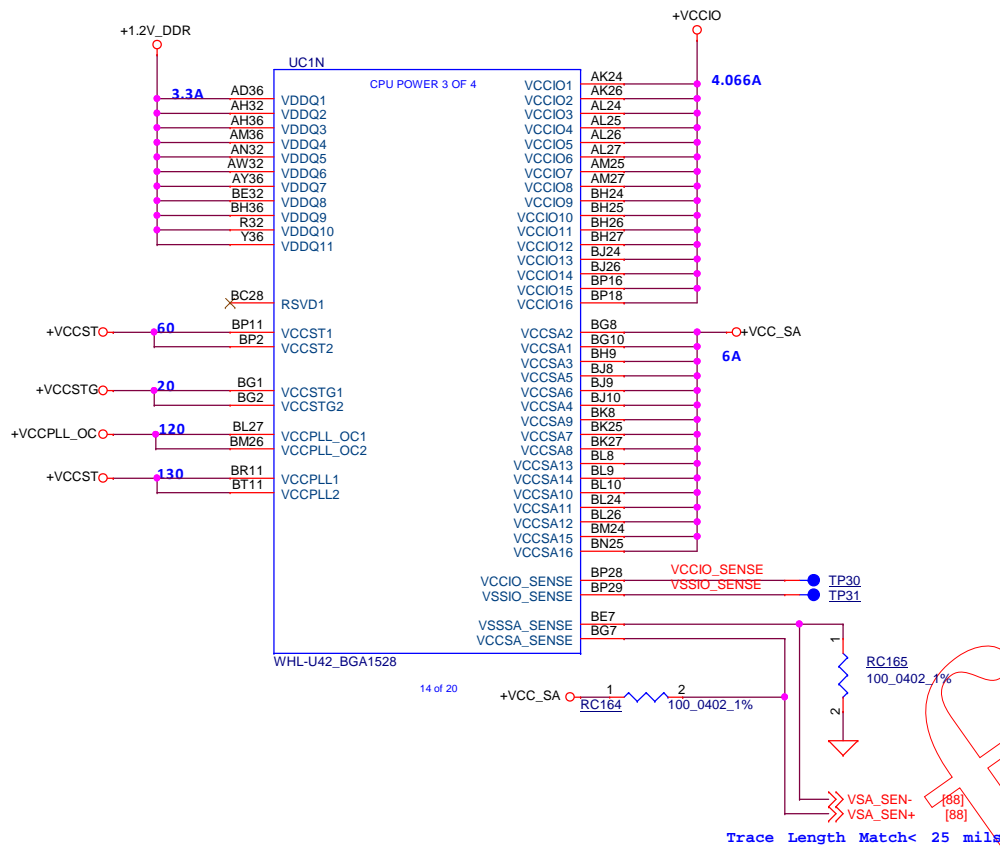
WHL-U42, BGA1528

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GPIO	DEVICE	CONTROL
USB_OC0#	USB	Port (MB)
USB_OC1#	USB	Port (DB)
USB_OC2#	NA	
USB_OC3#	USB	Port (Type-C)
DEVSLP0	HDD	
DEVSLP1	NA	
DEVSLP2	M.2 SSD	



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								Size		Document Number		Rev	
								Custom		LA-G716P		1.0	
								Date:		Thursday, June 20, 2019		Sheet 12 of 101	

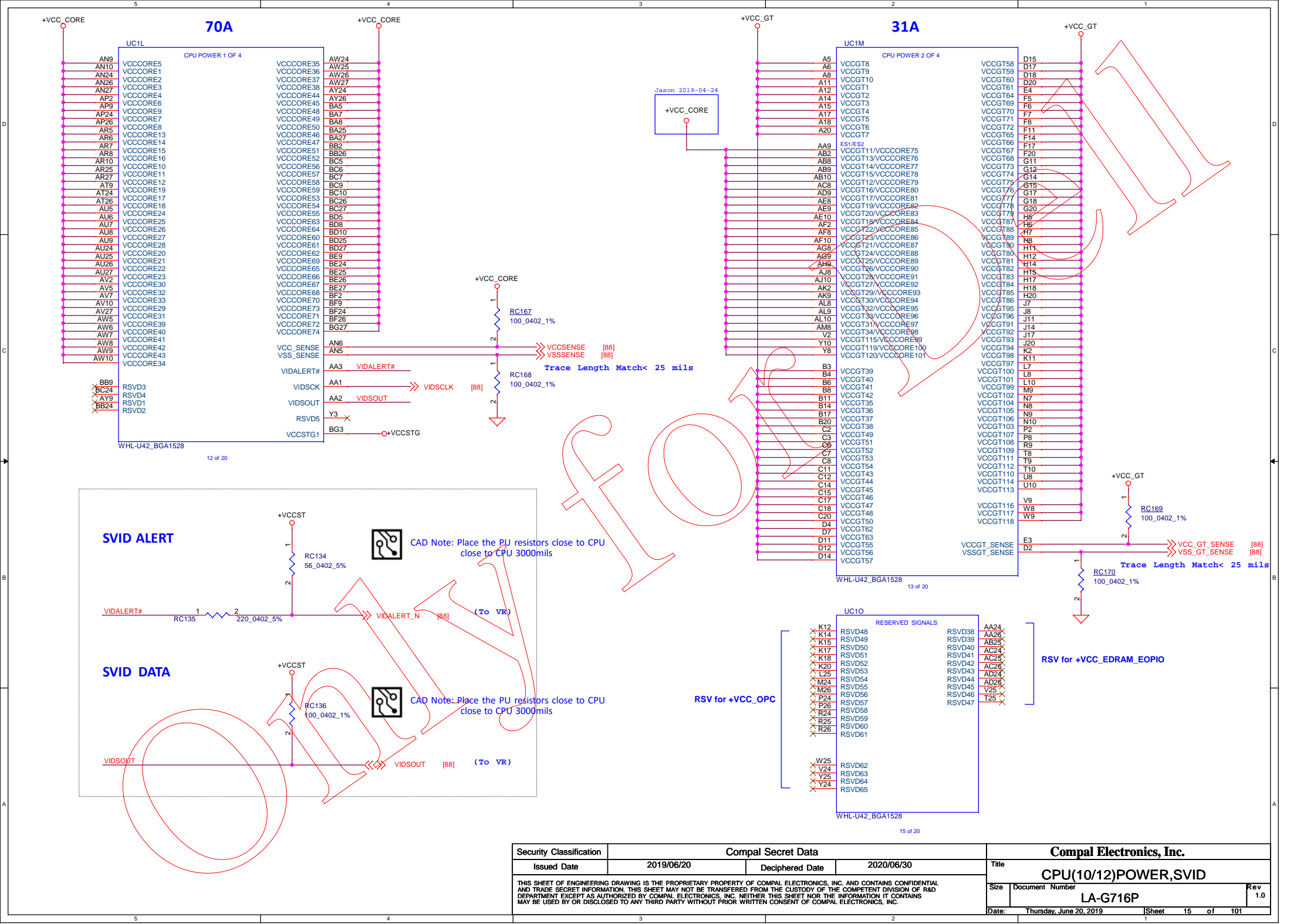


Trace Length Match< 25 mils

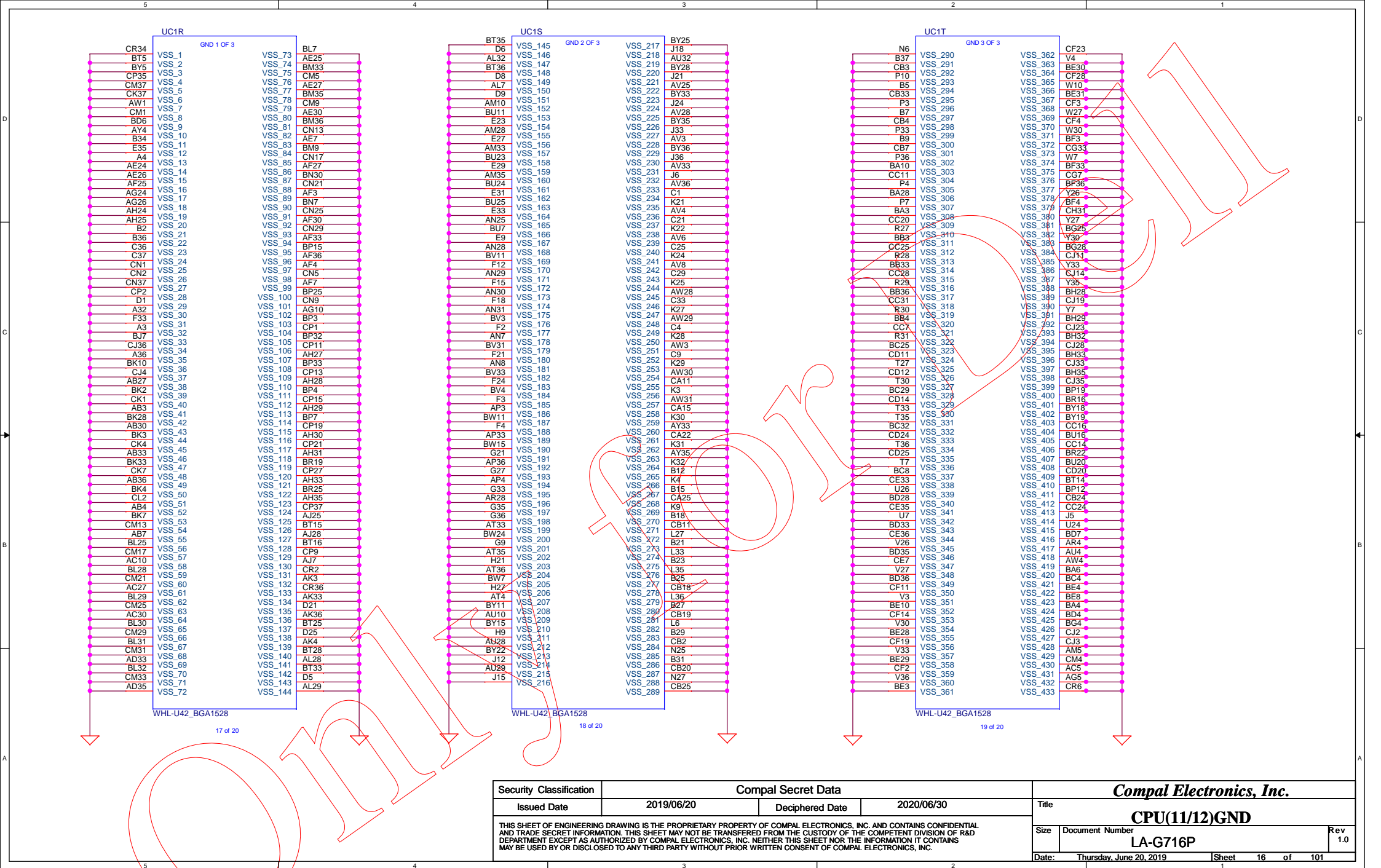
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2019/06/20	Deciphered Date	2020/06/30	Title	
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				Size	Rev
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								Size	Document Number	Rev
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									101	

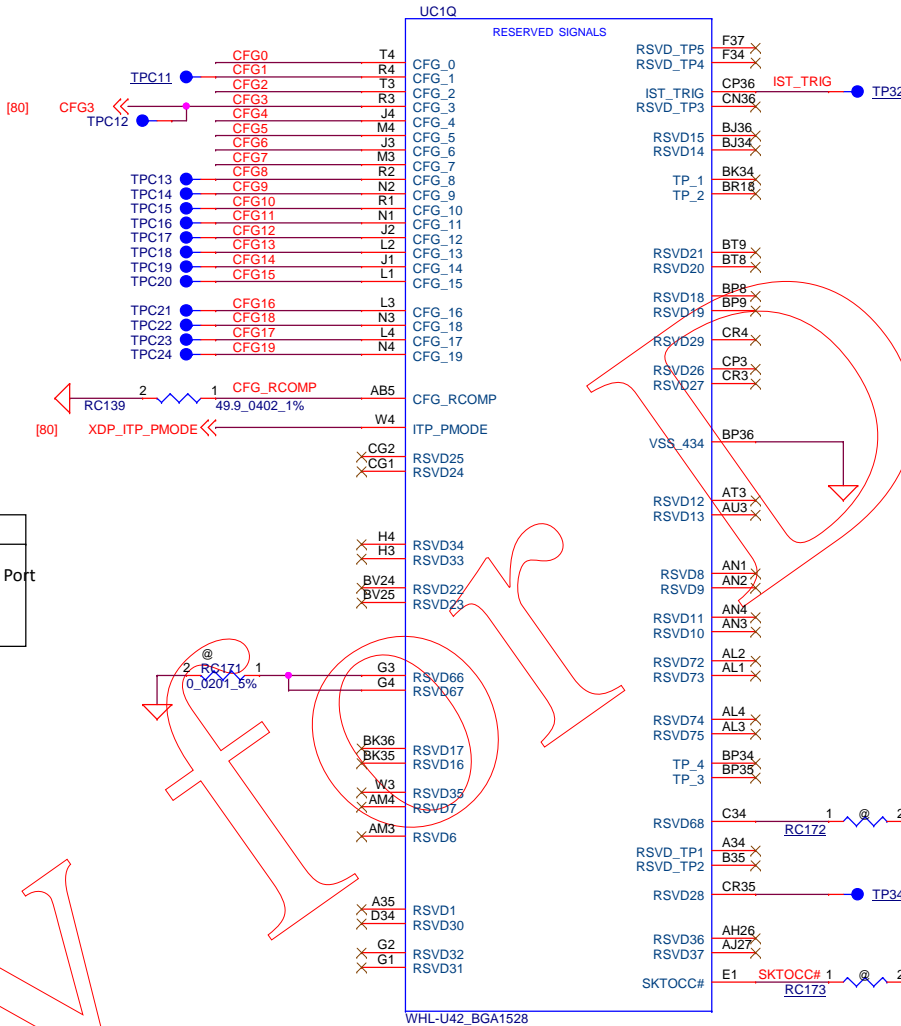
Reset sequence after PCU PLL is locked	
CFG0	1 : (DEFAULT) Normal Operation No Stall 0 : Stall

PCI Express Static Lane Reversal For All PEG Ports	
CFG2	1 : (DEFAULT) Normal Operation 0 : Lane Reversal

Display Port Presence Strap	
CFG4	0 : Enabled; An external Display Port device is connected to the Embedded Display Port 1 : Disabled; No Physical Display Port attached to Embedded Display Port

PCI Express Bifurcation	
CFG[6:5]	<ul style="list-style-type: none"> <li>00 = 1x8 2x4 PCI Express*</li> <li>01 = reserved</li> <li>10 = 2x8 PCI Express*</li> <li>11 = 1x16 PCI Express*</li> </ul>

PEG Training	
CFG7	1 : (default) PEG Train immediately following RESET# de-assertion 0 : PEG Wait for BIOS for training.



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				Size	Document Number
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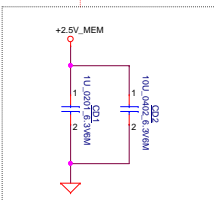
Reserve

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					LA-G716P	1.0
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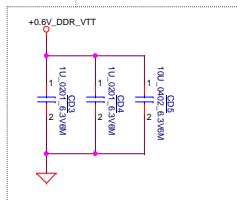


Main Func = DDR

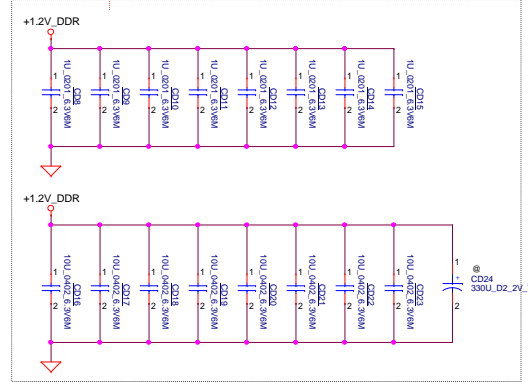
Layout Note:  
Place near JDIMM1.257,259



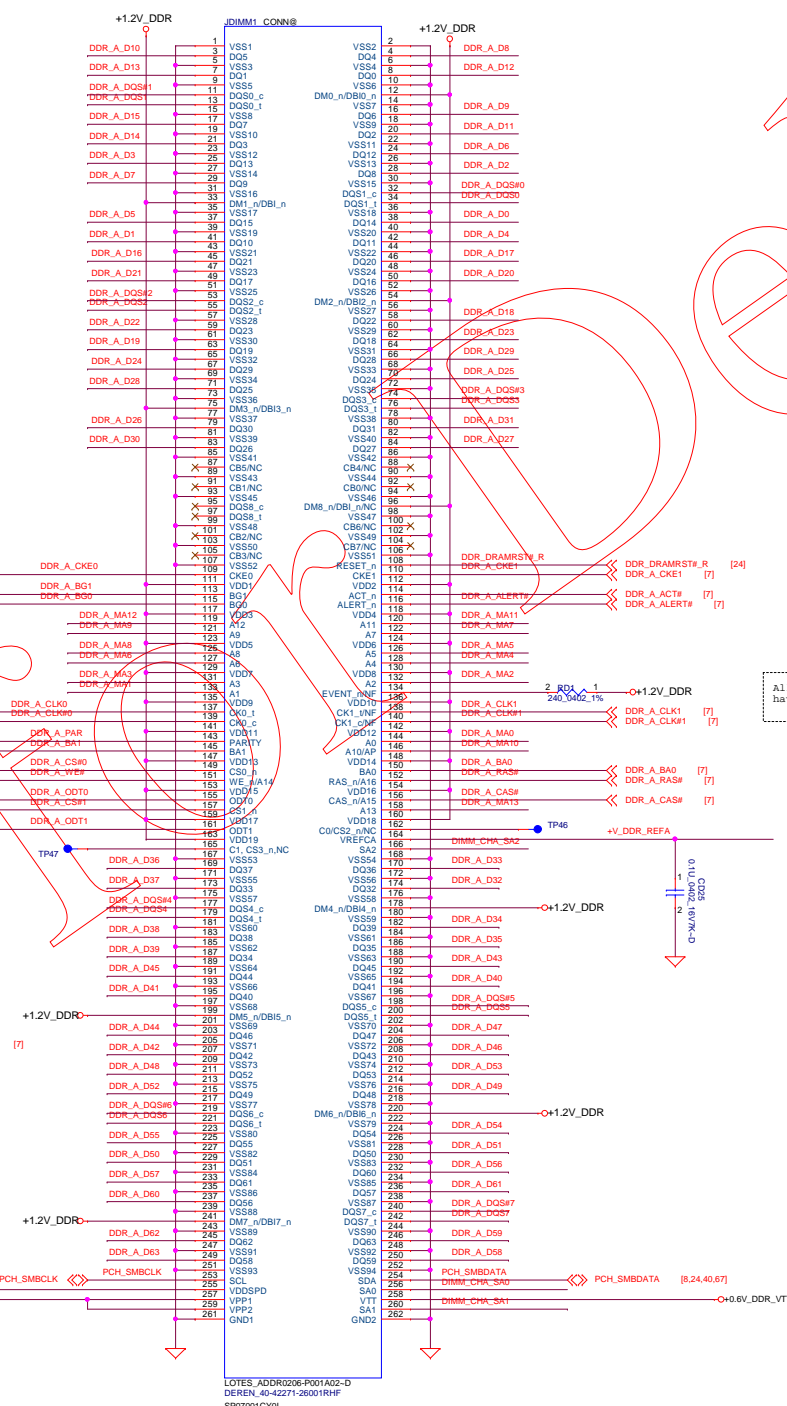
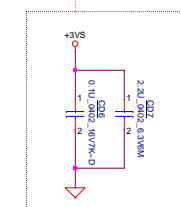
Layout Note:  
Place near JDIMM1.258



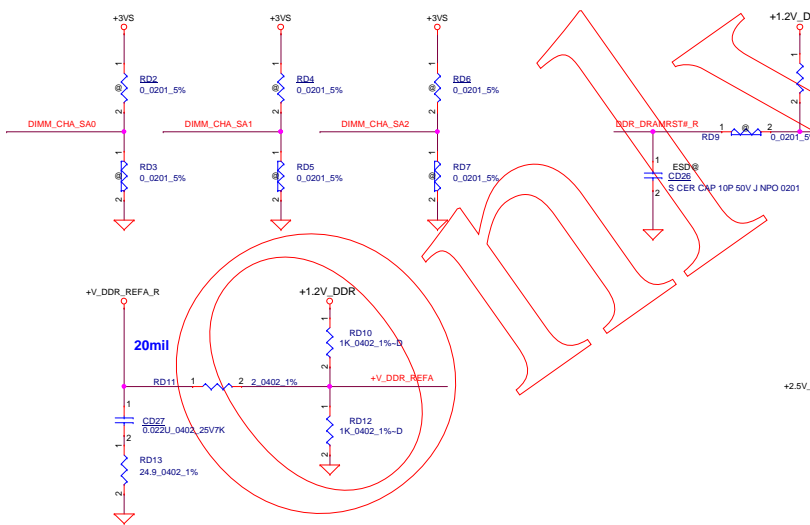
Layout Note:  
Place near JDIMM1



Layout Note:  
Place near JDIMM1.255



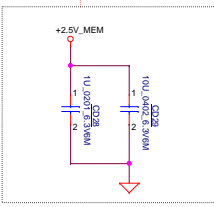
All VREF traces should  
have 10 mil trace width



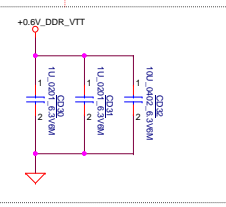
Security Classification		Compal Secret Data		Title	
Issued Date	2019/06/20	Deciphered Date	2020/06/30	DDR4 DIMM RVS	
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Main Func = DDR

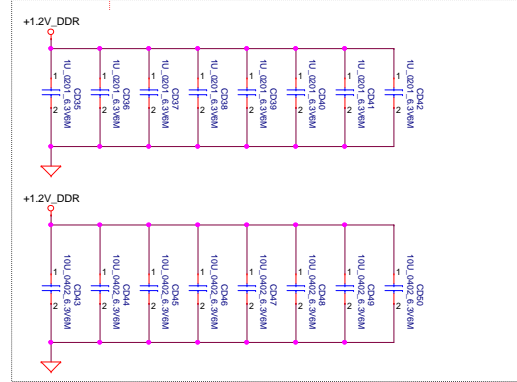
Layout Note:  
Place near JDIMM2.257,259



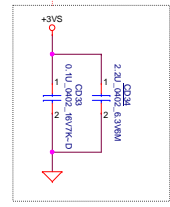
Layout Note:  
Place near JDIMM2.258



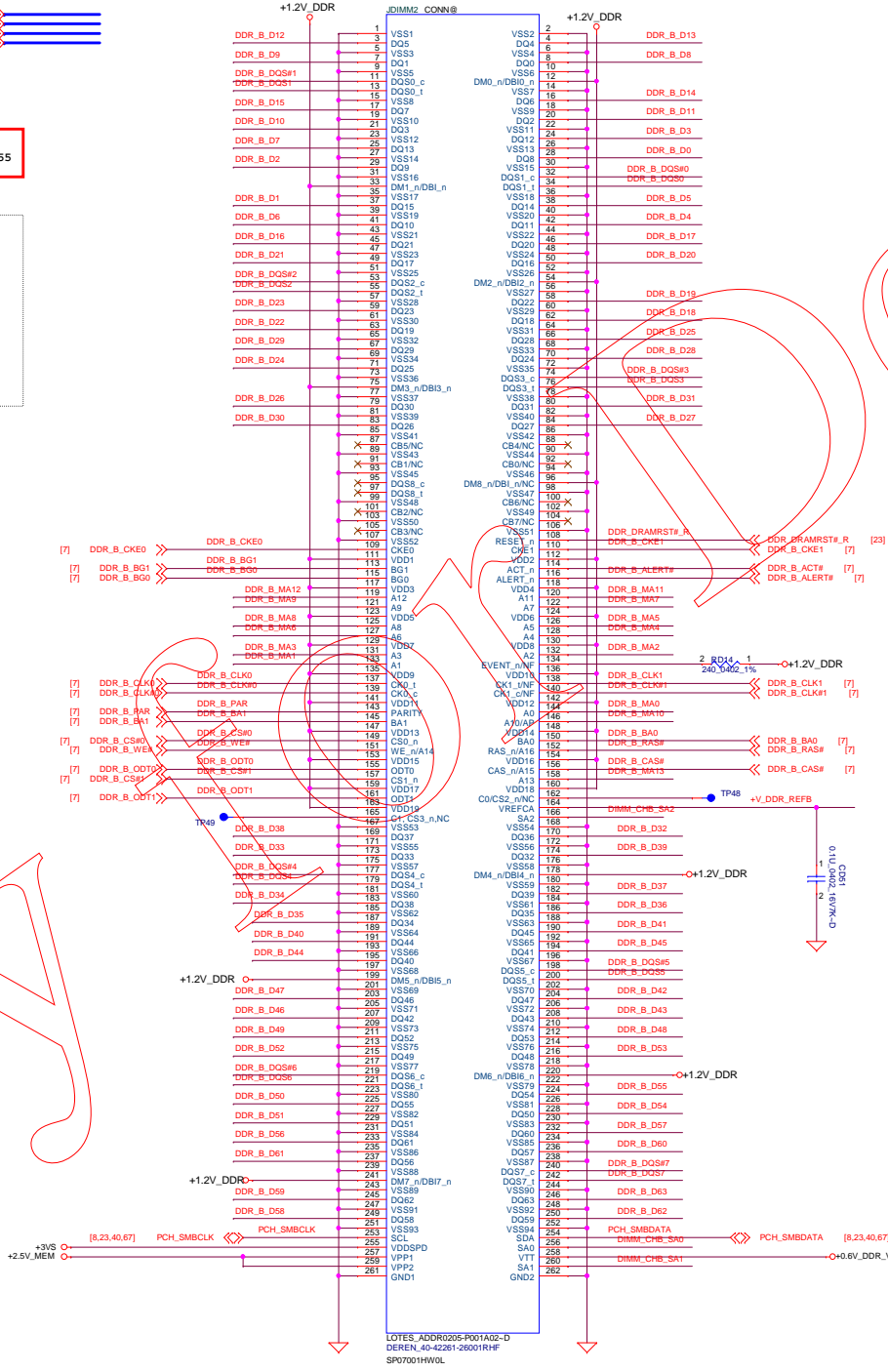
Layout Note:  
Place near JDIMM2



Layout Note:  
Place near JDIMM2.255



[7] DDR\_B\_D0[0..63]  
[7] DDR\_B\_MA0[13]  
[7] DDR\_B\_DQS0[0..7]  
[7] DDR\_B\_DQS0[0..7]



All VREF traces should  
have 10 mil trace width

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					LA-G716P	1.0
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Main Func = GPU



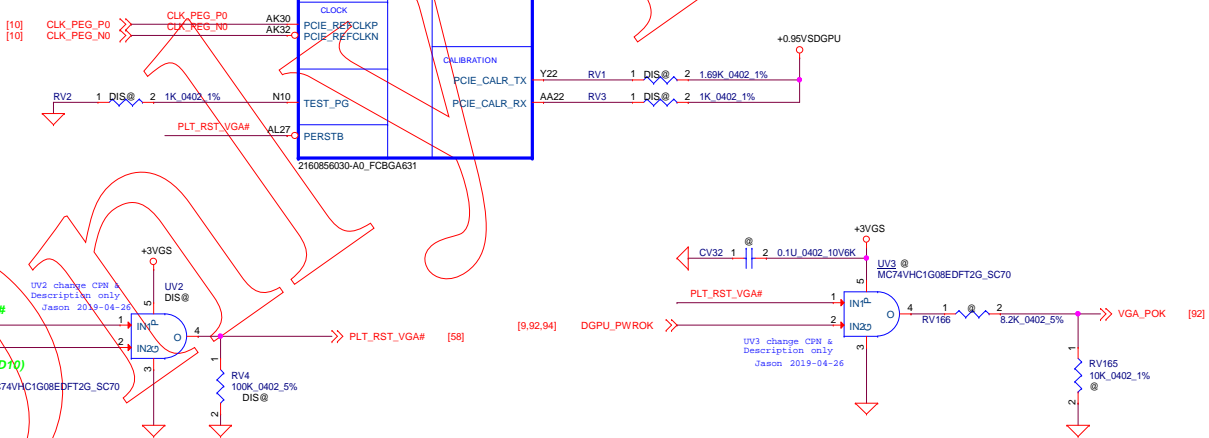
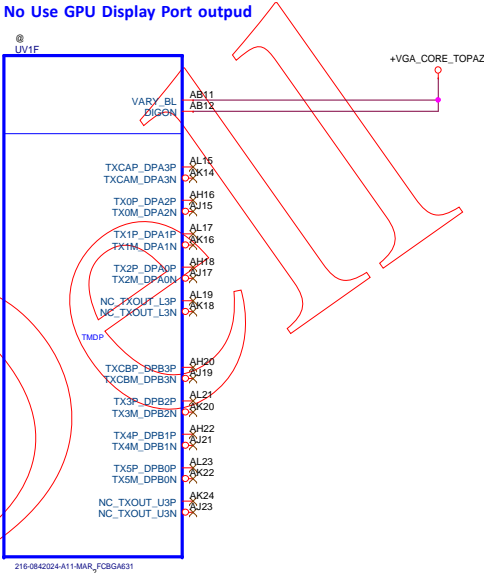
**GPU R1/R3**

UV1 SA0000B1W0L  
M2\_50@  
S IC 216-0889004 A0 R17M-M2-50 FCBGA 0FD

UV1 SA0000B1W1L  
M2\_50\_R3@  
S IC 216-0889004 A0 R17M-M2-50 WESTON XT BGA 631P GPU A31 !

UV1 R610 R1  
SA000087T5L  
M1\_30\_R1@  
S IC 216-0890010 A0 R17M-M1-30 631P R610

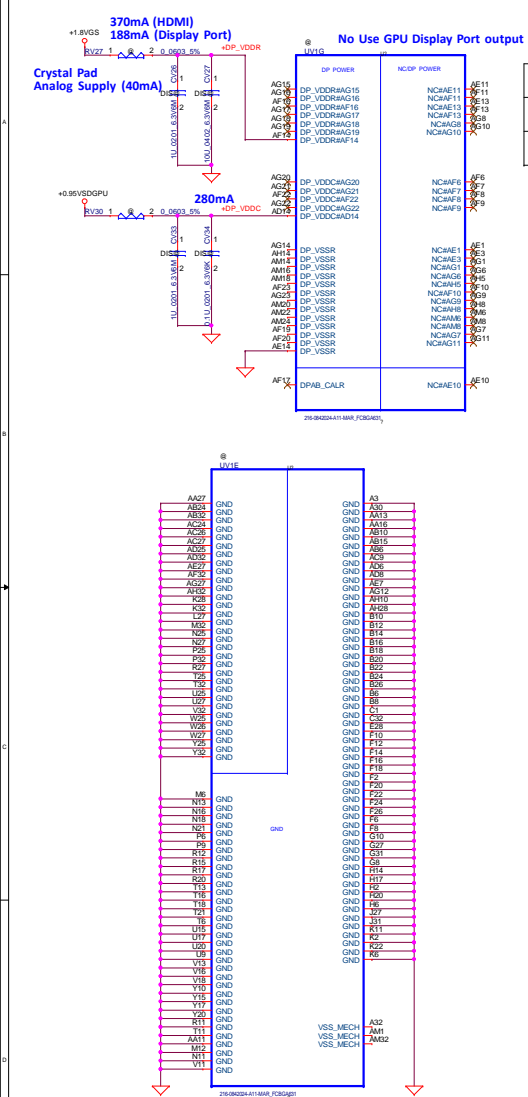
UV1 R610 R3  
SA000087T6L  
M1\_30\_R3@  
S IC 216-0890010 A0 R17M-M1-30 R610 A31!



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				Document Number	1.0
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**Main Func = GPU**



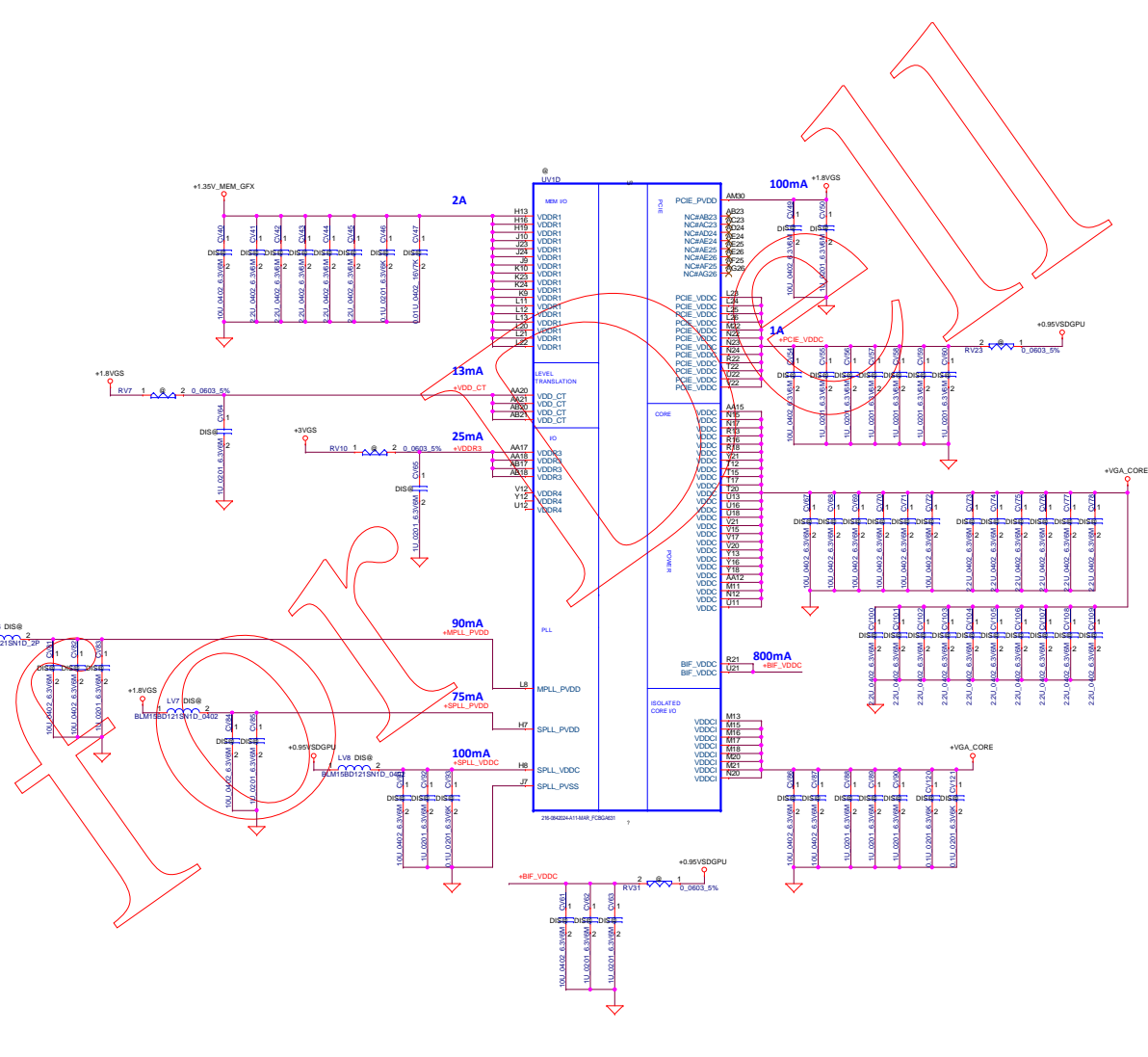
+VGA_CORE	10uF	2.2uF 1uF	0.1uF
VDDC	6	16	0
VDDCI	2	3	2

+0.9VSDGPU	10uF	1uF	0.1uF
PCIE_VDDC	1	6	0
BIF_VDDC	1	2	0
SPLL_VDDC	1	1	1
+DP_VDDC	0	1	1

+1.35V_MEM_GFX	10uF	2.2uF	0.1uF	0.01uF
VDDR1	1	5	1	1

+3VGS	10uF	1uF	0.1uF
VDDR3	0	1	0

+1.8VGS	10uF	1uF	0.1uF
PCIE_PVDD	1	1	0
MPLL_PVDD	2	1	0
SPLL_PVDD	1	1	0
VDDR4 (NC)	0	0	0
VDD_CT	0	1	0
+TSVDD	0	1	0
+DP_VDDR	1	1	0

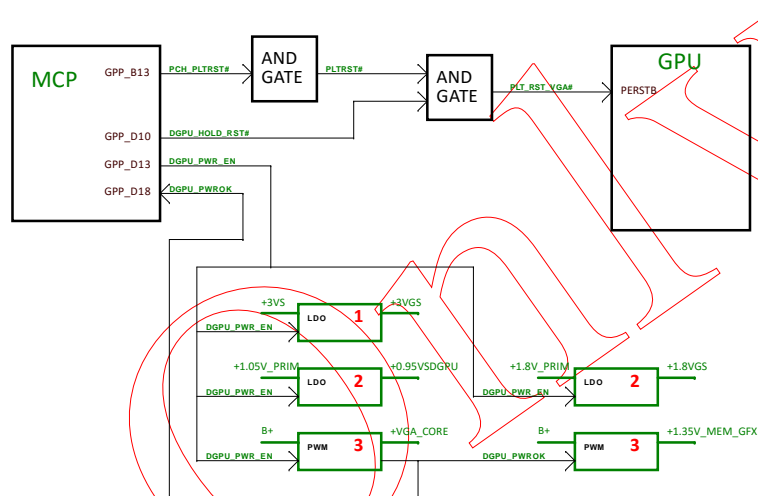
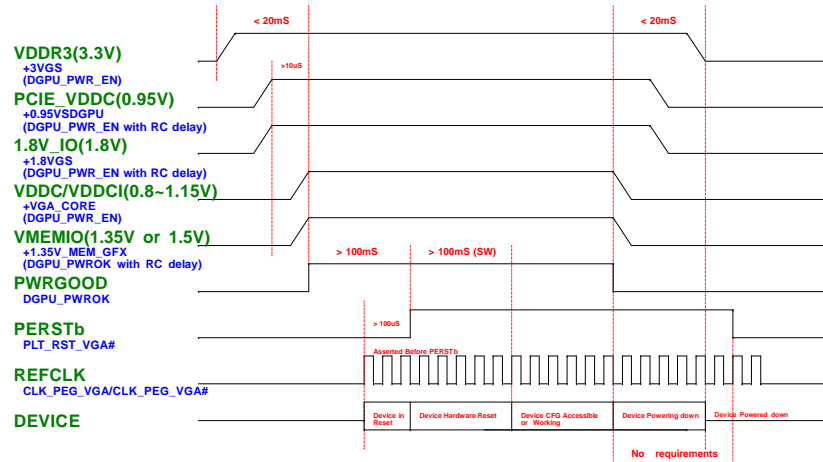


Security Classification	Compal Secret Data		Compal Electronics, Inc.	
	2019/06/20	Deciphered Date	2020/06/30	
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			Size	Document Number
			Date	LA-G716P
			Drawn	Thursday, June 20, 2019
			Check	29 of 101

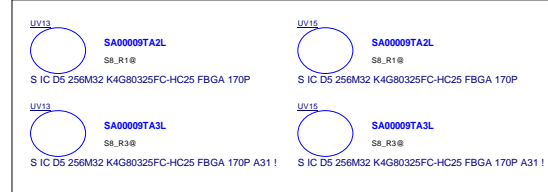


## Power-Up/Down Sequence

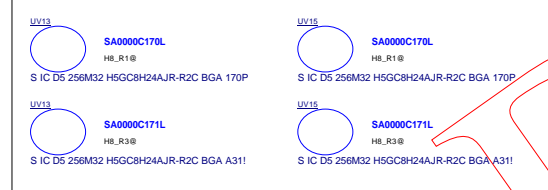
1. All the ASIC supplies must reach their respective nominal voltages within 20 ms of the start of the ramp-up sequence, though a shorter ramp-up duration is preferred. The maximum slew rate on all rails is 50 mV/ $\mu$ s.
2. It is recommended that the 3.3-V rail ramp up first.
3. It is recommended that the 0.95-V rail reach at least 90% of its nominal value no later than 2 ms from the start of VDDC ramping up.
4. The power rails that are shared with other components on the system should be gated for the dGPU so that when the dGPU is powered down (for example AMD PowerXpress? idle state), all the power rails are removed from the dGPU. The gate circuits must meet the slow rate requirement (such as 50 mV/ $\mu$ s).
5. VDDC and VDD\_CT should not ramp up simultaneously. For example, VDDC should reach 90% before VDD\_CT starts to ramp up (or vice versa).
6. For power down, reversing the ramp-up sequence is recommended.



## Samsung 2G



## Hynix 2G



## Micron 2G

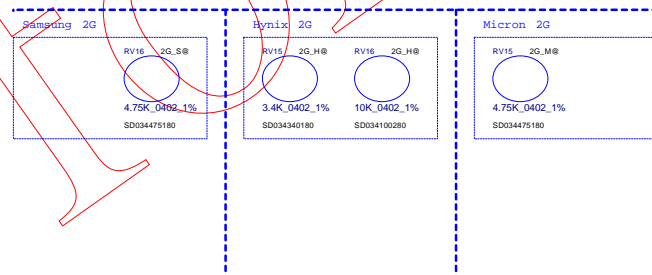
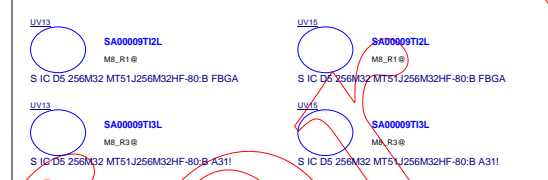


Table 3-21 Resistor Divider Lookup T.

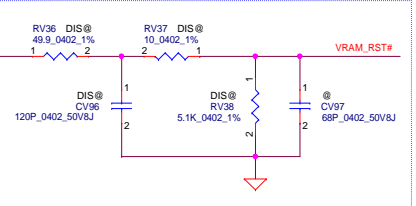
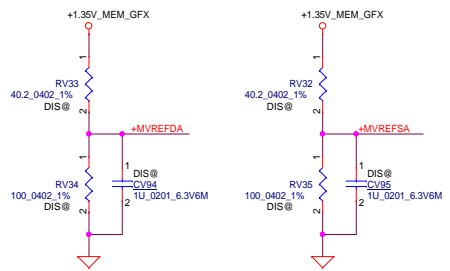
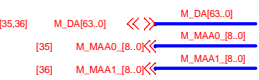
R <sub>pu</sub> (Ω)	R <sub>pd</sub> (Ω)	Bits [3:1]
NC	4750	000
8450	2000	001
4530	2000	010
6980	4990	011
4530	4990	100
3240	5620	101
3400	10000	110
4750	NC	111

Note: 0402 1% resistors are required.

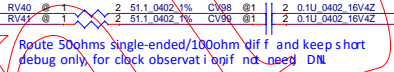
## For AMD R17M-M1-30 VRAM

Memory ID	8Gb R3 P/N	Vendor	Configuration	Size
000	SA00009TA3L	SAMSUNG	S IC D5 256M32 K4G80325FC-HC25 FBGA 170P A31 !	2GB
110	SA0000C171L	Hynix	S IC D5 256M32 H5GC8H24AJR-R2C BGA A31!	2GB
111	SA00009T13L	Micron	S IC D5 256M32 MT51J256M32HF-80:B A31!	2GB

Main Func = GPU



Place close to GPU (within 25mm)  
and place component close to each other



Route 50ohms single-ended/100ohm diff and keep short  
debug only for clock observat and keep short

DDR5/DDR3		DDR5/DDR3	
M_DA0	K27	MAA0_0/MAA_0	K17
M_DA1	J29	MAA0_1/MAA_1	J20
M_DA2	H30	MAA0_2/MAA_2	H23
M_DA3	H32	MAA0_3/MAA_3	G23
M_DA4	G29	MAA0_4/MAA_4	G24
M_DA5	F28	MAA0_5/MAA_5	F24
M_DA6	F32	MAA0_6/MAA_6	J18
M_DA7	F30	MAA0_7/MAA_7	K19
M_DA8	C30	MAA0_8/MAA_8	G20
M_DA9	F27	MAA0_9/MAA_9	L17
M_DA10	A28	MAA0_10/MAA_10	K
M_DA11	C28	MAA1_0/MAA_1	J14
M_DA12	E27	MAA1_1/MAA_2	J11
M_DA13	G26	MAA1_2/MAA_3	J13
M_DA14	D26	MAA1_3/MAA_4	H11
M_DA15	F25	MAA1_4/MAA_5	G11
M_DA16	A25	MAA1_5/MAA_6	J16
M_DA17	C25	MAA1_6/MAA_7	L15
M_DA18	D25	MAA1_7/MAA_8	G14
M_DA19	D24	MAA1_8/MAA_9	L16
M_DA20	E23	MAA1_9/RSVD	K
M_DA21	F23	WCKA0_0/DQMA0_0	E32
M_DA22	D22	WCKA0_1/DQMA0_1	E30
M_DA23	F21	WCKA0_2/DQMA0_2	A21
M_DA24	E21	WCKA0_3/DQMA0_3	C21
M_DA25	D21	WCKA0_4/DQMA0_4	E13
M_DA26	F19	WCKA0_5/DQMA0_5	D12
M_DA27	A19	WCKA0_6/DQMA0_6	E3
M_DA28	D18	WCKA0_7/DQMA0_7	F4
M_DA29	E17	WCKA0_8/DQMA0_8	H28
M_DA30	A17	WCKA0_9/DQMA0_9	C27
M_DA31	C17	WCKA0_10/DQMA0_10	A25
M_DA32	E17	WCKA0_11/DQMA0_11	E19
M_DA33	D16	WCKA0_12/DQMA0_12	E18
M_DA34	F15	WCKA0_13/DQMA0_13	D10
M_DA35	A15	WCKA0_14/DQMA0_14	D6
M_DA36	D14	WCKA0_15/DQMA0_15	G5
M_DA37	F13	WCKA0_16/DQMA0_16	H27
M_DA38	A13	WCKA0_17/DQMA0_17	C23
M_DA39	C13	WCKA0_18/DQMA0_18	C19
M_DA40	E11	WCKA0_19/DQMA0_19	C15
M_DA41	A11	WCKA0_20/DQMA0_20	E9
M_DA42	C11	WCKA0_21/DQMA0_21	H4
M_DA43	F11	WCKA0_22/DQMA0_22	C18
M_DA44	A9	WCKA0_23/DQMA0_23	K16
M_DA45	D9	WCKA0_24/DQMA0_24	H26
M_DA46	F9	WCKA0_25/DQMA0_25	G8
M_DA47	A7	WCKA0_26/DQMA0_26	H9
M_DA48	D7	WCKA0_27/DQMA0_27	G12
M_DA49	E7	WCKA0_28/DQMA0_28	G17
M_DA50	C7	WCKA0_29/DQMA0_29	G19
M_DA51	F7	WCKA0_30/DQMA0_30	G16
M_DA52	A5	WCKA0_31/DQMA0_31	H22
M_DA53	D5	WCKA0_32/DQMA0_32	G13
M_DA54	F5	WCKA0_33/DQMA0_33	K13
M_DA55	A3	WCKA0_34/DQMA0_34	K20
M_DA56	D3	WCKA0_35/DQMA0_35	J17
M_DA57	F3	WCKA0_36/DQMA0_36	G25
M_DA58	A1	WCKA0_37/DQMA0_37	H10
M_DA59	C1	WCKA0_38/DQMA0_38	
M_DA60	E1	WCKA0_39/DQMA0_39	
M_DA61	A0	WCKA0_40/DQMA0_40	
M_DA62	D0	WCKA0_41/DQMA0_41	
M_DA63	F0	WCKA0_42/DQMA0_42	
M_DA64	A0	WCKA0_43/DQMA0_43	
M_DA65	D0	WCKA0_44/DQMA0_44	
M_DA66	F0	WCKA0_45/DQMA0_45	
M_DA67	A0	WCKA0_46/DQMA0_46	
M_DA68	D0	WCKA0_47/DQMA0_47	
M_DA69	F0	WCKA0_48/DQMA0_48	
M_DA70	A0	WCKA0_49/DQMA0_49	
M_DA71	D0	WCKA0_50/DQMA0_50	
M_DA72	F0	WCKA0_51/DQMA0_51	
M_DA73	A0	WCKA0_52/DQMA0_52	
M_DA74	D0	WCKA0_53/DQMA0_53	
M_DA75	F0	WCKA0_54/DQMA0_54	
M_DA76	A0	WCKA0_55/DQMA0_55	
M_DA77	D0	WCKA0_56/DQMA0_56	
M_DA78	F0	WCKA0_57/DQMA0_57	
M_DA79	A0	WCKA0_58/DQMA0_58	
M_DA80	D0	WCKA0_59/DQMA0_59	
M_DA81	F0	WCKA0_60/DQMA0_60	
M_DA82	A0	WCKA0_61/DQMA0_61	
M_DA83	D0	WCKA0_62/DQMA0_62	
M_DA84	F0	WCKA0_63/DQMA0_63	
M_DA85	A0	WCKA0_64/DQMA0_64	
M_DA86	D0	WCKA0_65/DQMA0_65	
M_DA87	F0	WCKA0_66/DQMA0_66	
M_DA88	A0	WCKA0_67/DQMA0_67	
M_DA89	D0	WCKA0_68/DQMA0_68	
M_DA90	F0	WCKA0_69/DQMA0_69	
M_DA91	A0	WCKA0_70/DQMA0_70	
M_DA92	D0	WCKA0_71/DQMA0_71	
M_DA93	F0	WCKA0_72/DQMA0_72	
M_DA94	A0	WCKA0_73/DQMA0_73	
M_DA95	D0	WCKA0_74/DQMA0_74	
M_DA96	F0	WCKA0_75/DQMA0_75	
M_DA97	A0	WCKA0_76/DQMA0_76	
M_DA98	D0	WCKA0_77/DQMA0_77	
M_DA99	F0	WCKA0_78/DQMA0_78	
M_DA100	A0	WCKA0_79/DQMA0_79	
M_DA101	D0	WCKA0_80/DQMA0_80	
M_DA102	F0	WCKA0_81/DQMA0_81	
M_DA103	A0	WCKA0_82/DQMA0_82	
M_DA104	D0	WCKA0_83/DQMA0_83	
M_DA105	F0	WCKA0_84/DQMA0_84	
M_DA106	A0	WCKA0_85/DQMA0_85	
M_DA107	D0	WCKA0_86/DQMA0_86	
M_DA108	F0	WCKA0_87/DQMA0_87	
M_DA109	A0	WCKA0_88/DQMA0_88	
M_DA110	D0	WCKA0_89/DQMA0_89	
M_DA111	F0	WCKA0_90/DQMA0_90	
M_DA112	A0	WCKA0_91/DQMA0_91	
M_DA113	D0	WCKA0_92/DQMA0_92	
M_DA114	F0	WCKA0_93/DQMA0_93	
M_DA115	A0	WCKA0_94/DQMA0_94	
M_DA116	D0	WCKA0_95/DQMA0_95	
M_DA117	F0	WCKA0_96/DQMA0_96	
M_DA118	A0	WCKA0_97/DQMA0_97	
M_DA119	D0	WCKA0_98/DQMA0_98	
M_DA120	F0	WCKA0_99/DQMA0_99	
M_DA121	A0	WCKA0_100/DQMA0_100	
M_DA122	D0	WCKA0_101/DQMA0_101	
M_DA123	F0	WCKA0_102/DQMA0_102	
M_DA124	A0	WCKA0_103/DQMA0_103	
M_DA125	D0	WCKA0_104/DQMA0_104	
M_DA126	F0	WCKA0_105/DQMA0_105	
M_DA127	A0	WCKA0_106/DQMA0_106	
M_DA128	D0	WCKA0_107/DQMA0_107	
M_DA129	F0	WCKA0_108/DQMA0_108	
M_DA130	A0	WCKA0_109/DQMA0_109	
M_DA131	D0	WCKA0_110/DQMA0_110	
M_DA132	F0	WCKA0_111/DQMA0_111	
M_DA133	A0	WCKA0_112/DQMA0_112	
M_DA134	D0	WCKA0_113/DQMA0_113	
M_DA135	F0	WCKA0_114/DQMA0_114	
M_DA136	A0	WCKA0_115/DQMA0_115	
M_DA137	D0	WCKA0_116/DQMA0_116	
M_DA138	F0	WCKA0_117/DQMA0_117	
M_DA139	A0	WCKA0_118/DQMA0_118	
M_DA140	D0	WCKA0_119/DQMA0_119	
M_DA141	F0	WCKA0_120/DQMA0_120	
M_DA142	A0	WCKA0_121/DQMA0_121	
M_DA143	D0	WCKA0_122/DQMA0_122	
M_DA144	F0	WCKA0_123/DQMA0_123	
M_DA145	A0	WCKA0_124/DQMA0_124	
M_DA146	D0	WCKA0_125/DQMA0_125	
M_DA147	F0	WCKA0_126/DQMA0_126	
M_DA148	A0	WCKA0_127/DQMA0_127	
M_DA149	D0	WCKA0_128/DQMA0_128	
M_DA150	F0	WCKA0_129/DQMA0_129	
M_DA151	A0	WCKA0_130/DQMA0_130	
M_DA152	D0	WCKA0_131/DQMA0_131	
M_DA153	F0	WCKA0_132/DQMA0_132	
M_DA154	A0	WCKA0_133/DQMA0_133	
M_DA155	D0	WCKA0_134/DQMA0_134	
M_DA156	F0	WCKA0_135/DQMA0_135	
M_DA157	A0	WCKA0_136/DQMA0_136	
M_DA158	D0	WCKA0_137/DQMA0_137	
M_DA159	F0	WCKA0_138/DQMA0_138	
M_DA160	A0	WCKA0_139/DQMA0_139	
M_DA161	D0	WCKA0_140/DQMA0_140	
M_DA162	F0	WCKA0_141/DQMA0_141	
M_DA163	A0	WCKA0_142/DQMA0_142	
M_DA164	D0	WCKA0_143/DQMA0_143	
M_DA165	F0	WCKA0_144/DQMA0_144	
M_DA166	A0	WCKA0_145/DQMA0_145	
M_DA167	D0	WCKA0_146/DQMA0_146	
M_DA168	F0	WCKA0_147/DQMA0_147	
M_DA169	A0	WCKA0_148/DQMA0_148	
M_DA170	D0	WCKA0_149/DQMA0_149	
M_DA171	F0	WCKA0_150/DQMA0_150	
M_DA172	A0	WCKA0_151/DQMA0_151	
M_DA173	D0	WCKA0_152/DQMA0_152	
M_DA174	F0	WCKA0_153/DQMA0_153	
M_DA175	A0	WCKA0_154/DQMA0_154	
M_DA176	D0	WCKA0_155/DQMA0_155	
M_DA177	F0	WCKA0_156/DQMA0_156	
M_DA178	A0	WCKA0_157/DQMA0_157	
M_DA179	D0	WCKA0_158/DQMA0_158	
M_DA180	F0	WCKA0_159/DQMA0_159	
M_DA181	A0	WCKA0_160/DQMA0_160	
M_DA182	D0	WCKA0_161/DQMA0_161	
M_DA183	F0	WCKA0_162/DQMA0_162	
M_DA184	A0	WCKA0_163/DQMA0_163	
M_DA185	D0	WCKA0_164/DQMA0_164	
M_DA186	F0	WCKA0_165/DQMA0_165	
M_DA187	A0	WCKA0_166/DQMA0_166	
M_DA188	D0	WCKA0_167/DQMA0_167	
M_DA189	F0	WCKA0_168/DQMA0_168	
M_DA190	A0	WCKA0_169/DQMA0_169	
M_DA191	D0	WCKA0_170/DQMA0_170	
M_DA192	F0	WCKA0_171/DQMA0_171	
M_DA193	A0	WCKA0_172/DQMA0_172	
M_DA194	D0	WCKA0_173/DQMA0_173	
M_DA195	F0	WCKA0_174/DQMA0_174	
M_DA196	A0	WCKA0_175/DQMA0_175	
M_DA197	D0	WCKA0_176/DQMA0_176	
M_DA198	F0	WCKA0_177/DQMA0_177	
M_DA199	A0	WCKA0_178/DQMA0_178	
M_DA200	D0	WCKA0_179/DQMA0_179	
M_DA201	F0	WCKA0_180/DQMA0_180	
M_DA202	A0	WCKA0_181/DQMA0_181	
M_DA203	D0	WCKA0_182/DQMA0_182	
M_DA204	F0	WCKA0_183/DQMA0_183	
M_DA205	A0	WCKA0_184/DQMA0_184	
M_DA206	D0	WCKA0_185/DQMA0_185	
M_DA207	F0	WCKA0_186/DQMA0_186	
M_DA208	A0	WCKA0_187/DQMA0_187	
M_DA209	D0	WCKA0_188/DQMA0_188	
M_DA210	F0	WCKA0_189/DQMA0_189	
M_DA211	A0	WCKA0_190/DQMA0_190	
M_DA212	D0	WCKA0_191/DQMA0_191	
M_DA213	F0	WCKA0_192/DQMA0_192	
M_DA214	A0	WCKA0_193/DQMA0_193	
M_DA215	D0	WCKA0_194/DQMA0_194	
M_DA216	F0	WCKA0_195/DQMA0_195	
M_DA217	A0	WCKA0_196/DQMA0_196	
M_DA218	D0	WCKA0_197/DQMA0_197	
M_DA219	F0	WCKA0_198/DQMA0_198	
M_DA220	A0	WCKA0_199/DQMA0_199	
M_DA221	D0	WCKA0_200/DQMA0_200	
M_DA222	F0	WCKA0_201/DQMA0_201	
M_DA223	A0	WCKA0_202/DQMA0_202	
M_DA224	D0	WCKA0_203/DQMA0_203	
M_DA225	F0	WCKA0_204/DQMA0_204	
M_DA226	A0	WCKA0_205/DQMA0_205	
M_DA227	D0	WCKA0_206/DQMA0_206	
M_DA228	F0	WCKA0_207/DQMA0_207	
M_DA229	A0	WCKA0_208/DQMA0_208	
M_DA230	D0	WCKA0_209/DQMA0_209	
M_DA231	F0	WCKA0_210/DQMA0_210	
M_DA232	A0	WCKA0_211/DQMA0_211	
M_DA233	D0	WCKA0_212/DQMA0_212	
M_DA234	F0	WCKA0_213/DQMA0_213	
M_DA235	A0	WCKA0_214/DQMA0_214	
M_DA236	D0	WCKA0_215/DQMA0_215	
M_DA237	F0	WCKA0_216/DQMA0_216	
M_DA238	A0	WCKA0_217/DQMA0_217	
M_DA239	D0	WCKA0_218/DQMA0_218	
M_DA240	F0	WCKA0_219/DQMA0_219	
M_DA241	A0	WCKA0_220/DQMA0_220	
M_DA242	D0	WCKA0_221/DQMA0_221	
M_DA243	F0	WCKA0_222/DQMA0_222	
M_DA244	A0	WCKA0_223/DQMA0_223	
M_DA245	D0	WCKA0_224/DQMA0_224	
M_DA246	F0	WCKA0_225/DQMA0_225	
M_DA247	A0	WCKA0_226/DQMA0_226	
M_DA248	D0	WCKA0_227/DQMA0_227	
M_DA249	F0	WCKA0_228/DQMA0_228	
M_DA250	A0	WCKA0_229/DQMA0_229	
M_DA251	D0	WCKA0_230/DQMA0_230	
M_DA252	F0	WCKA0_231/DQMA0_231	
M_DA253	A0	WCKA0_232/DQMA0_232	
M_DA254	D0	WCKA0_233/DQMA0_233	
M_DA255	F0	WCKA0_234/DQMA0_234	
M_DA256	A0	WCKA0_235/DQMA0_235	
M_DA257	D0	WCKA0_236/DQMA0_236	
M_DA258	F0	WCKA0_237/DQMA0_237	
M_DA259	A0	WCKA0_238/DQMA0_238	
M_DA260	D0	WCKA0_239/DQMA0_239	
M_DA261	F0	WCKA0_240/DQMA0_240	
M_DA262	A0	WCKA0_241/DQMA0_241	
M_DA263	D0	WCKA0_242/DQMA0_242	
M_DA264	F0	WCKA0_243/DQMA0_243	
M_DA265	A0	WCKA0_244/DQMA0_244	
M_DA266	D0	WCKA0_245/DQMA0_245	
M_DA267	F0	WCKA0_246/DQMA0_246	
M_DA268	A0	WCKA0_247/DQMA0_247	
M_DA269	D0	WCKA0_248/DQMA0_248	
M_DA270	F0	WCKA0_249/DQMA0_249	
M_DA271	A0	WCKA0_250/DQMA0_250	
M_DA272	D0	WCKA0_251/DQMA0_251	</

Main Function:

Reserve

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					LA-G716P	1.0
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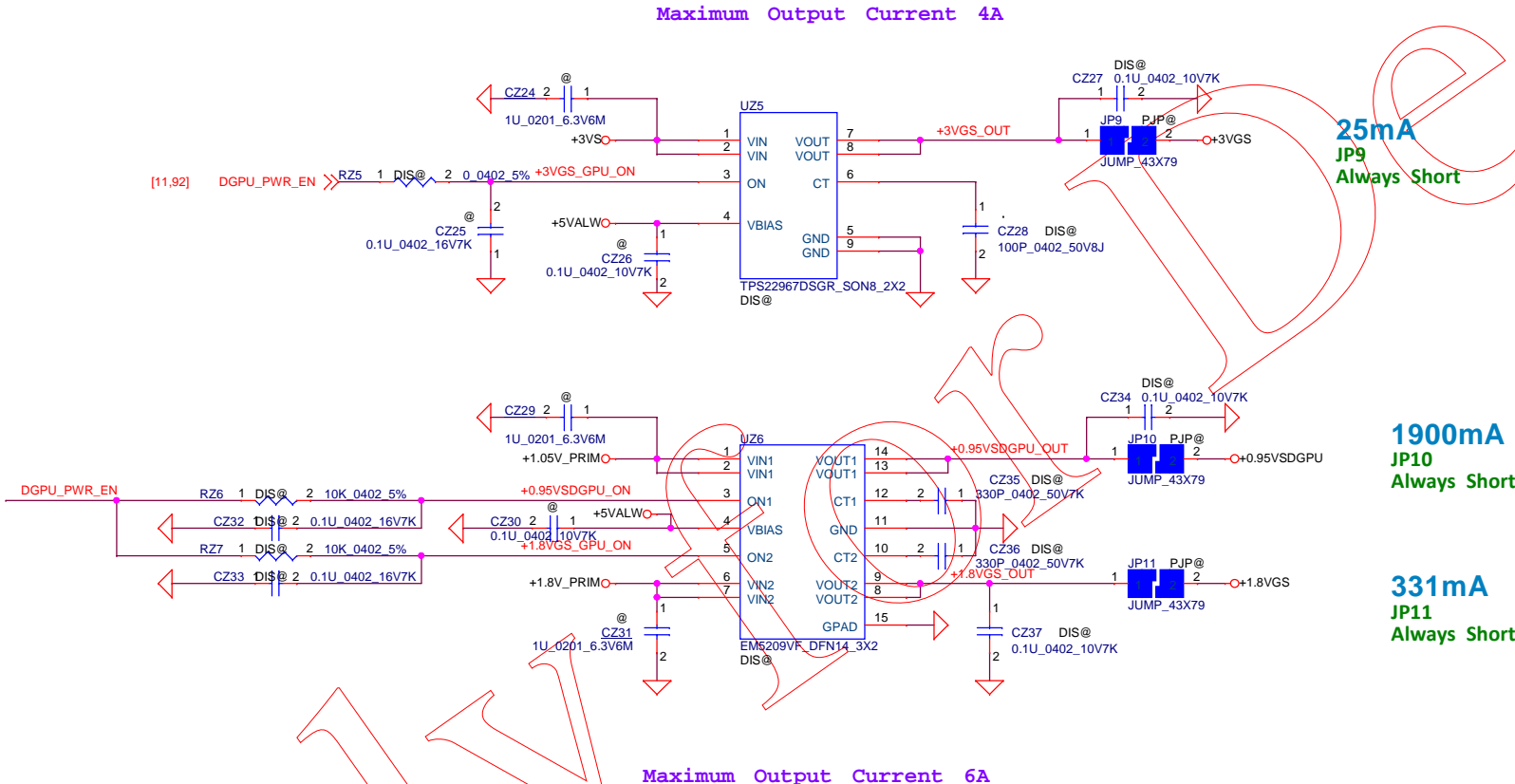






Main Func = GPU

+3V/+0.95V/+1.8V for GPU



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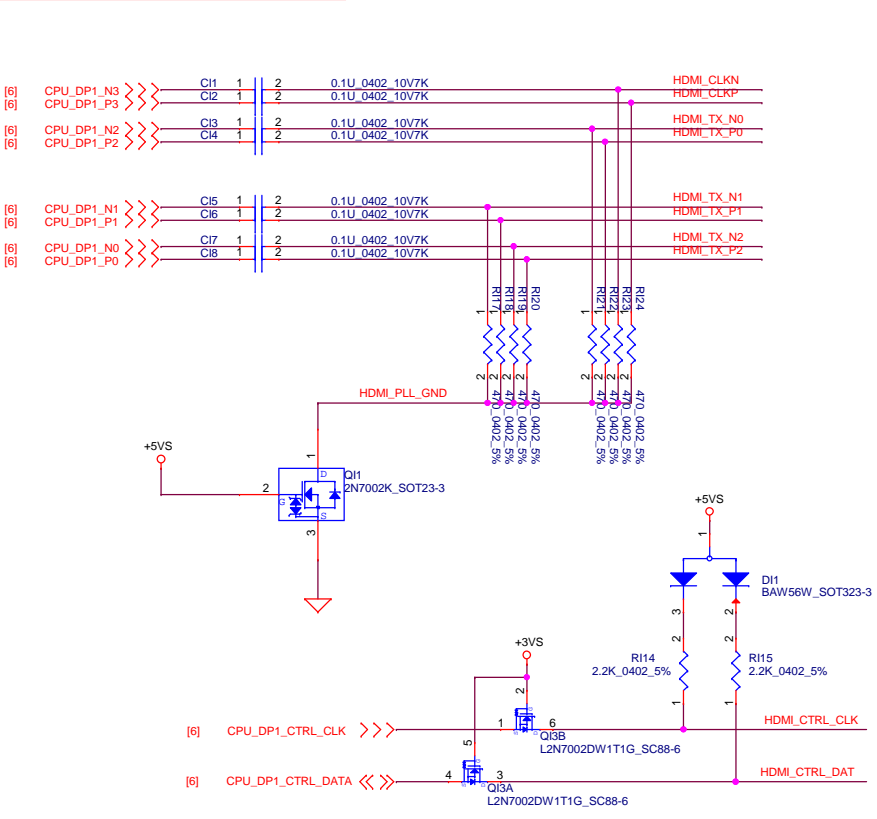


Main Function:

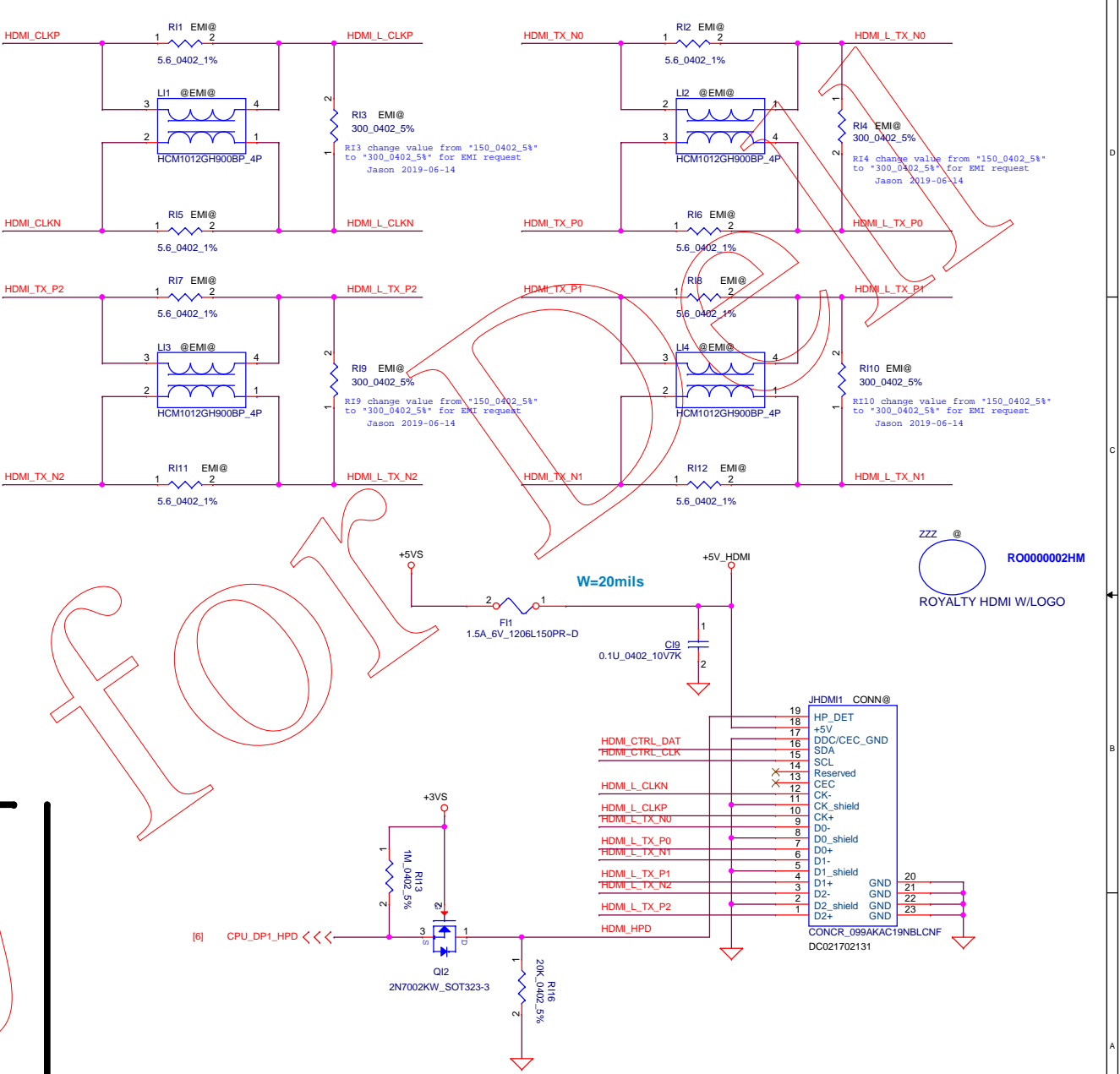
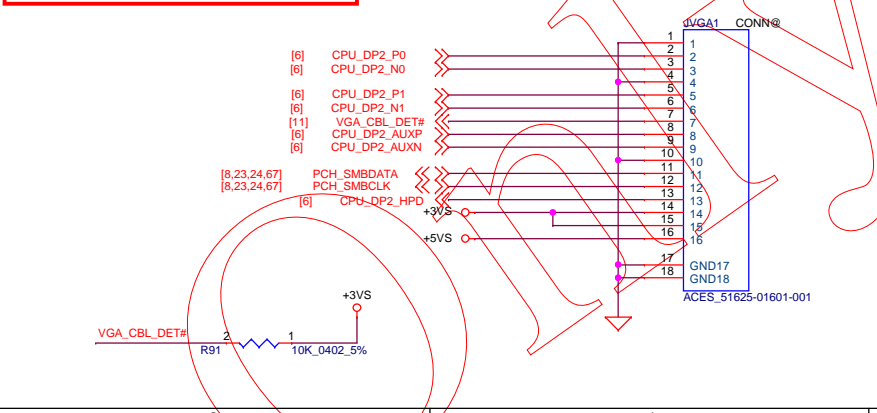
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Main Func = HDMI



Main Func = VGA



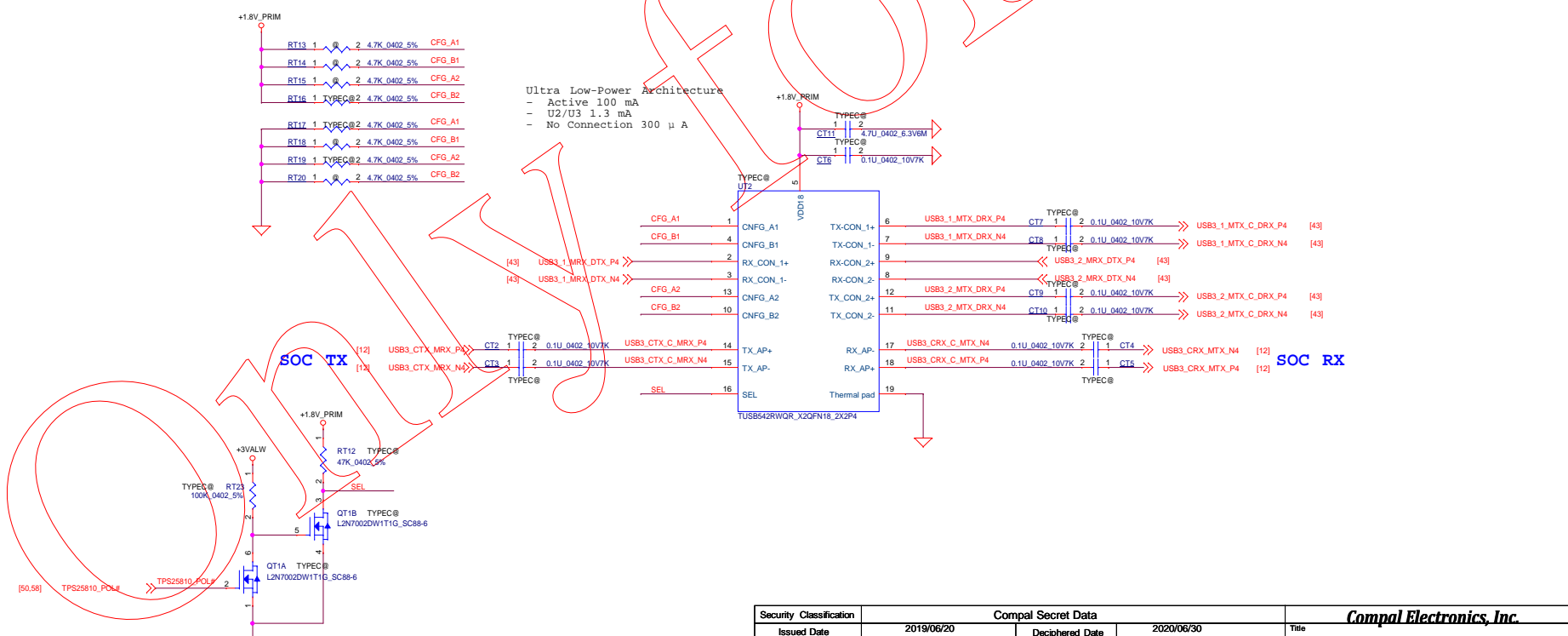
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Issued Date	2019/06/20	Deciphered Date	2020/06/30	Title	
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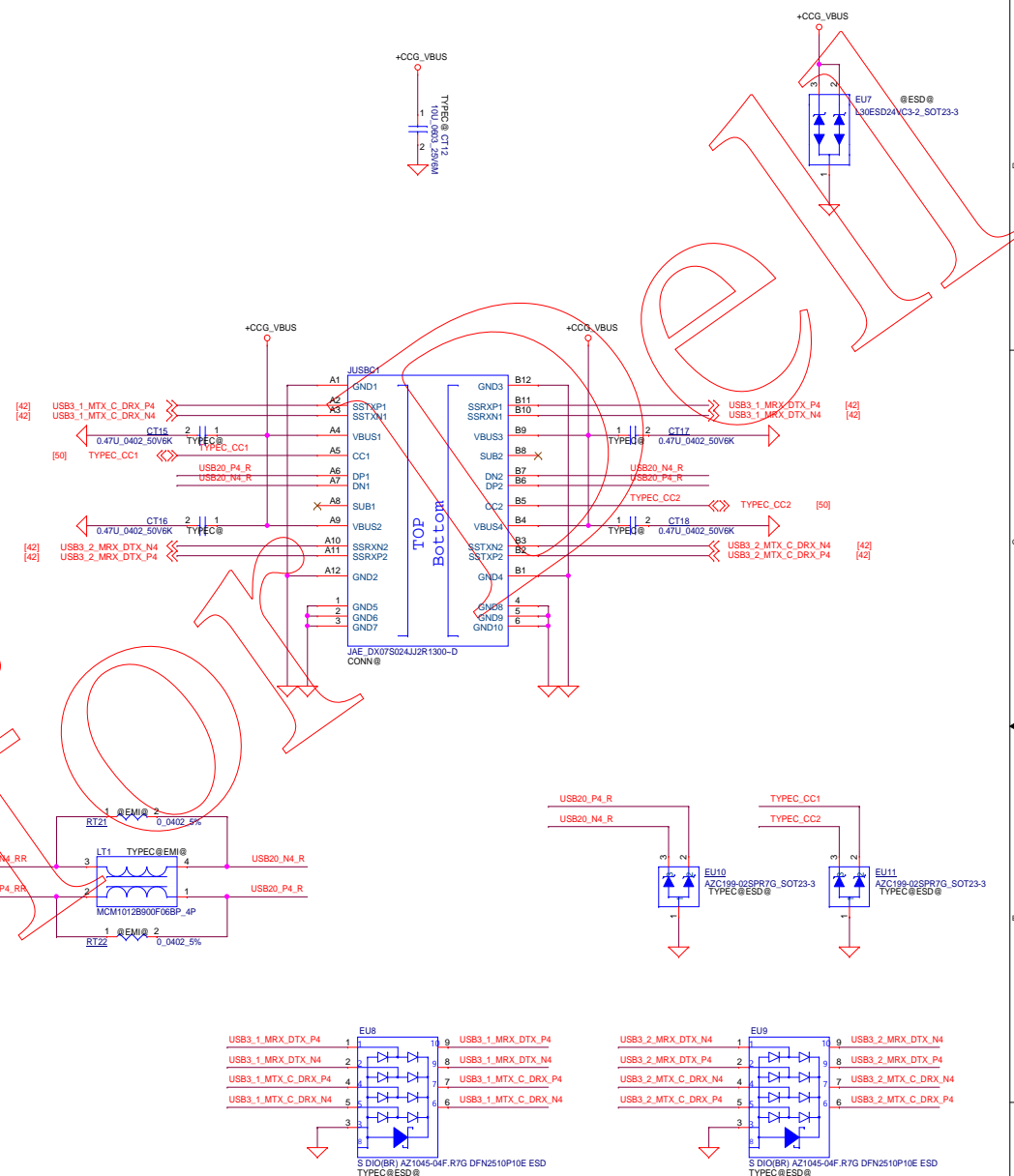
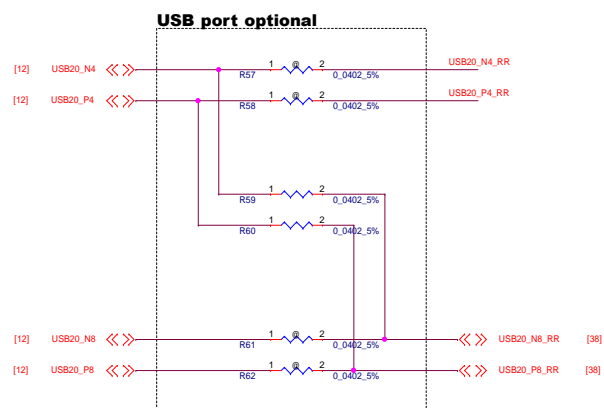
Main Func = Typec MUX



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5  
Main Func = USB2 Mux



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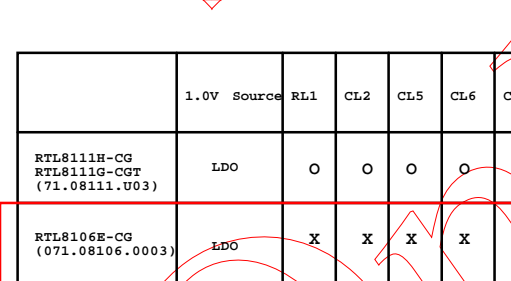
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				Size	Document Number	Rev
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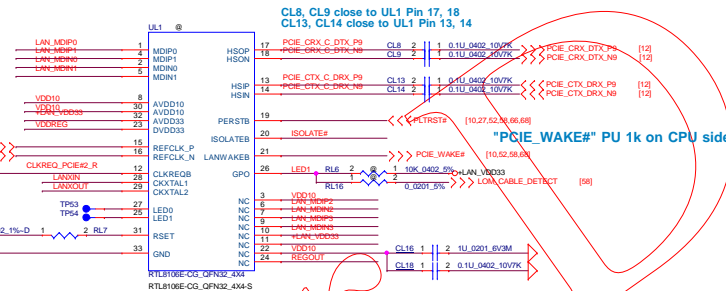
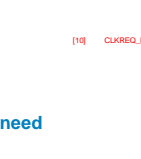
## Main Func = LAN



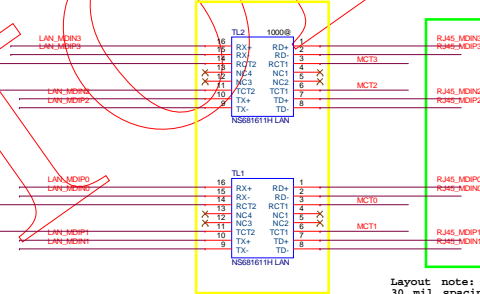
RTL8106E-CG (071.08106.0003)	LDO	X	X	X	X	X	O
---------------------------------	-----	---	---	---	---	---	---

**LAN CHIP 10/100/1000**

RTL8111N-CG	RTL8106E-CG
SA000080P00	SA000065Y00
LDO mode	LDO mode
10/100/1000M	10/100M



**Main Func = LAN** LAN TransFormer 10/100M x2

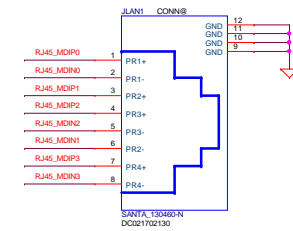
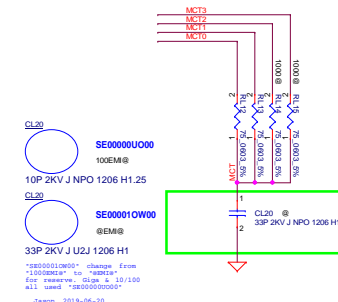


Layout note:  
30 mil spacing between MDI differential pairs.

Layout note:  
30 mil spacing between MDI differential pairs.

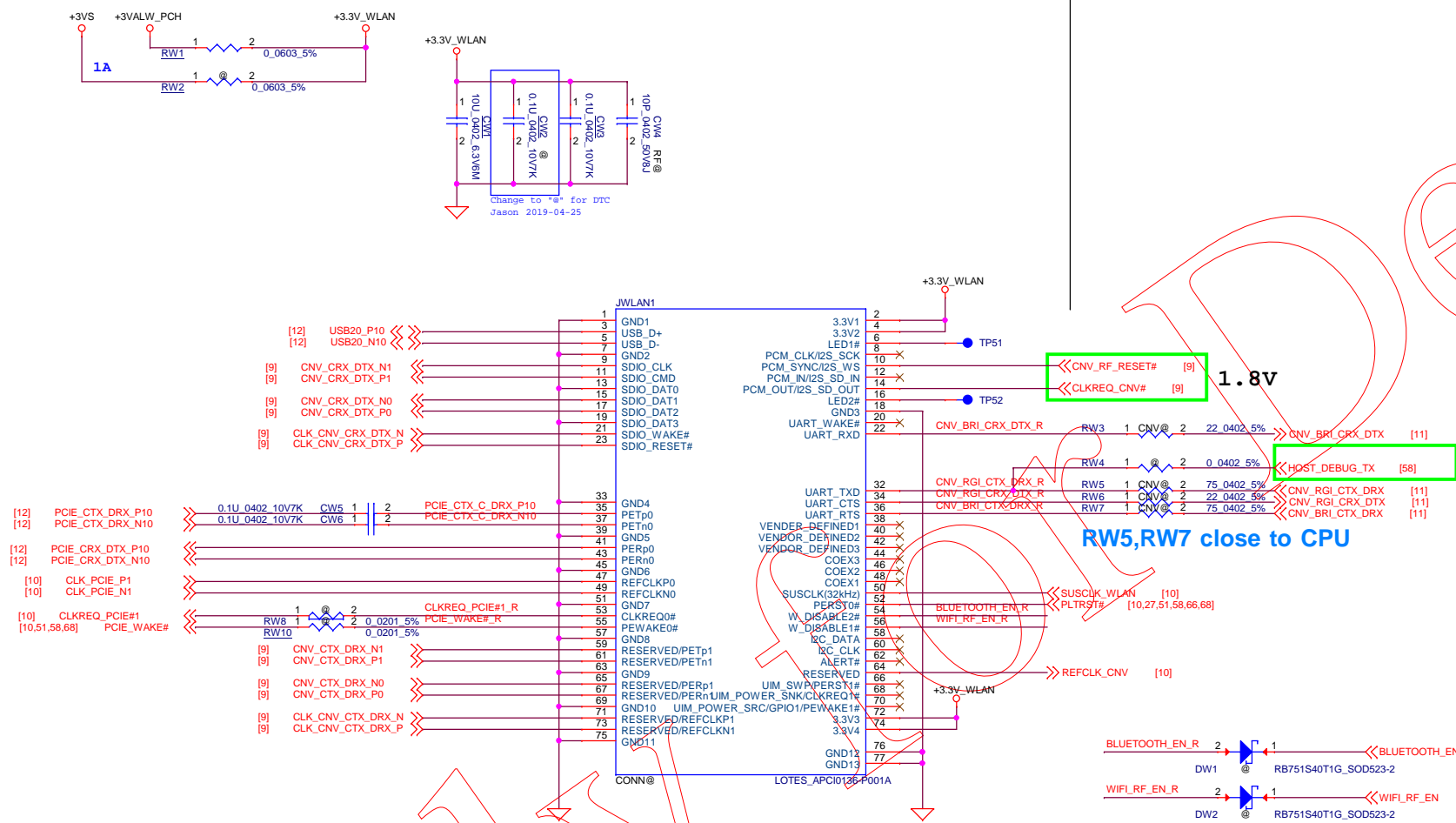
Follow Reference Schematic 0.01uF-0.4uF

Main:  
SP050006H00, S X'FORM\_ NS0014 LF LAN  
2nd:  
SP050006W00, S X'FORM\_ HD-245 10/100 PC CARD LAN



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Main Func = WLAN E Key CONN



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				1.0		
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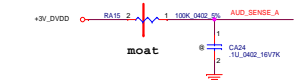
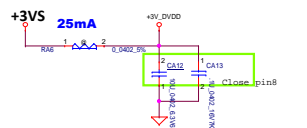
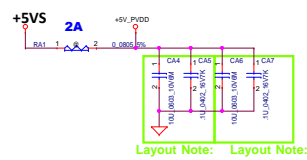
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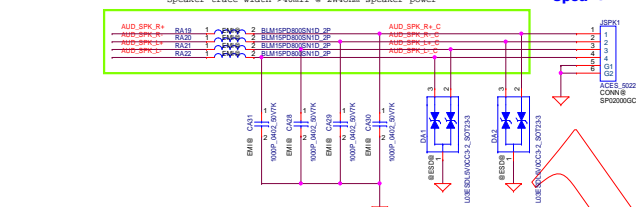
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					LA-G716P	1.0
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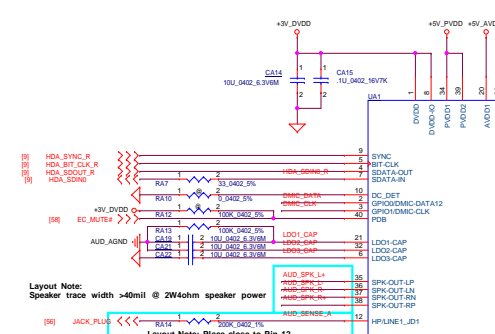
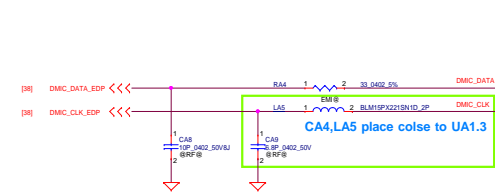
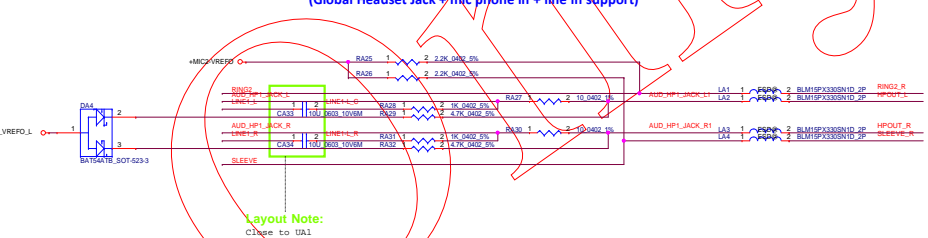


**Layout Note:**  
Speaker trace width >40mil @ 2W4ohm speaker power



Main Func = Audio Jack

Universal Jack  
(Global Headset Jack + mic phone in + line in support)



**Layout Note:**  
Place close to Pin 12

**Layout Note:**  
Place on the moat between GND & GNDA.

**Layout Note:**  
Tied at point only under

**Layout Note:**  
Add 2 vias (>0.5A) when trace layer change.

**Layout Note:**  
Change R to share will be better.

**Layout Note:**  
Width=40mil to improve Headphone Crosstalk noise

**Layout Note:**  
Change R to share will be better.

**Layout Note:**  
Add 2 vias (>0.5A) when trace layer change.

**Layout Note:**  
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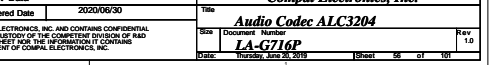
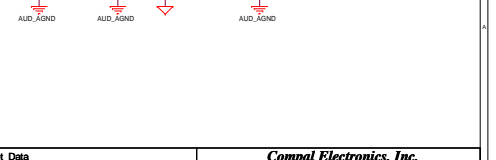
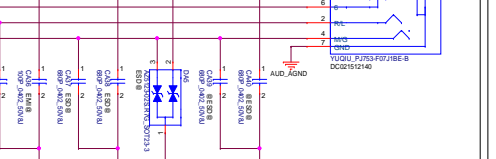
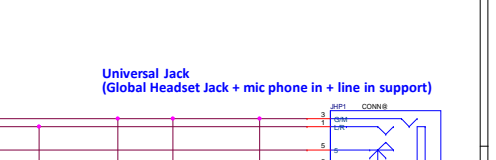
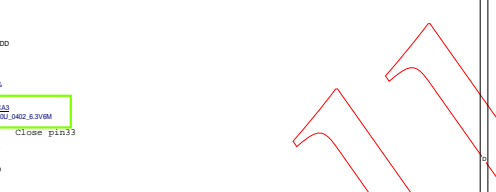
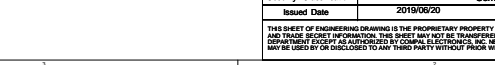
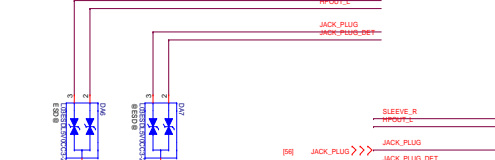
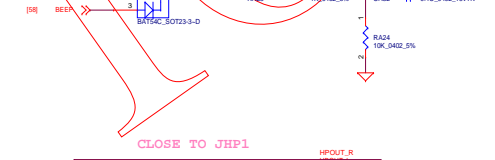
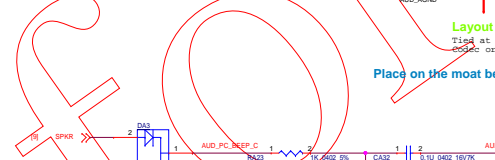
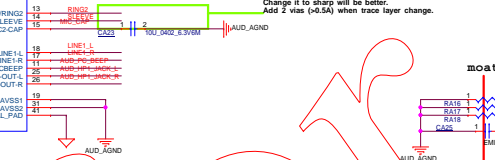
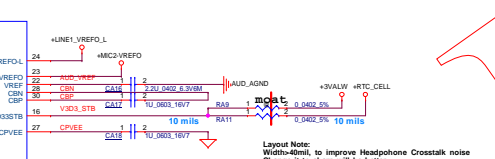
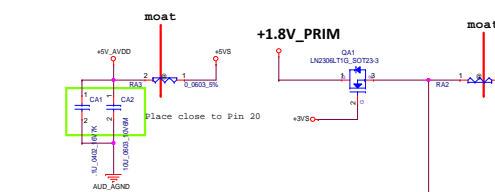
**Layout Note:**  
Change R to share will be better.

**Layout Note:**  
Width=40mil to improve Headphone Crosstalk noise

**Layout Note:**  
Change R to share will be better.

**Layout Note:**  
Add 2 vias (>0.5A) when trace layer change.

**Layout Note:**  
Change R to share will be better.



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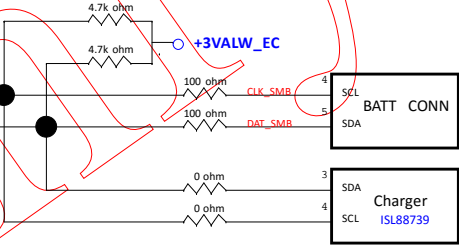
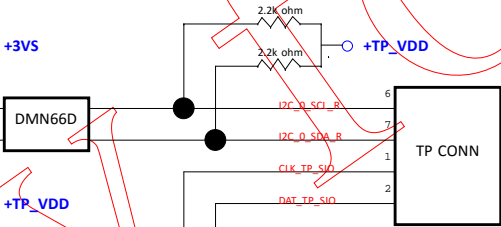
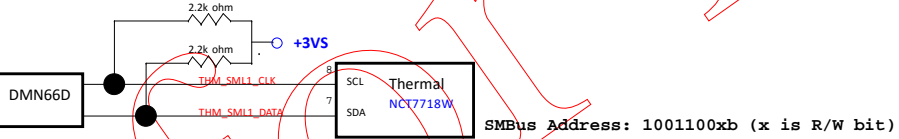
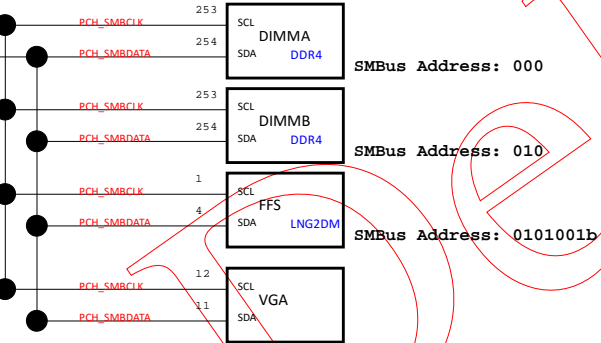
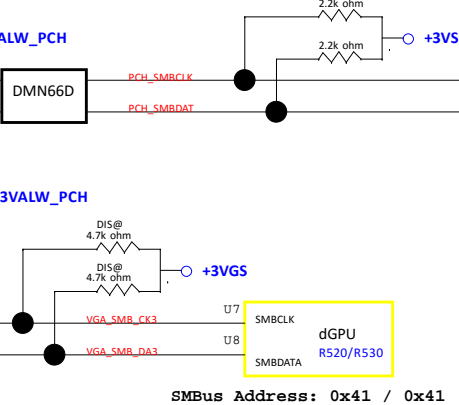
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Main Function:SMB/I2C Block Diagrams



Main Function:

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Main Func = KI



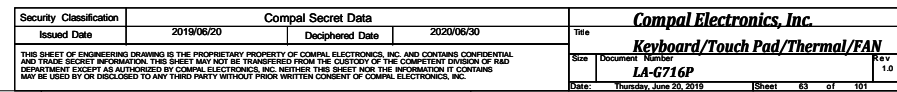
## Main Func = TPAD



Main Func = Battery LED

Low activated from KBC GPIO

BJT  
R1: 47 K  
R2: 10 K



Main Function:

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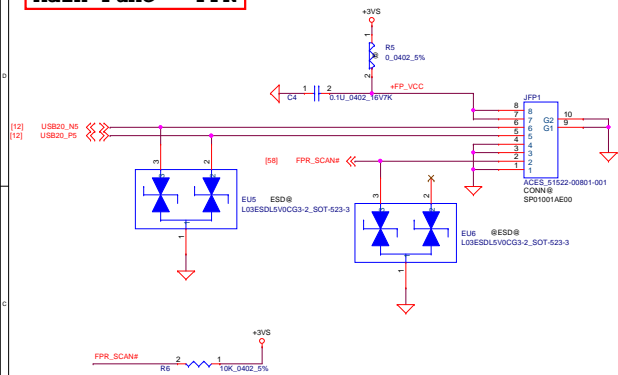
Main Function:

Reserve

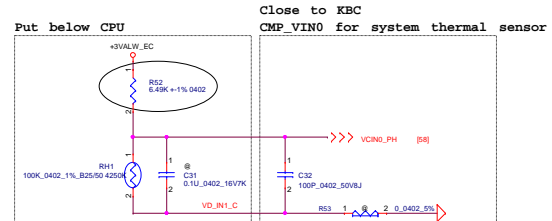
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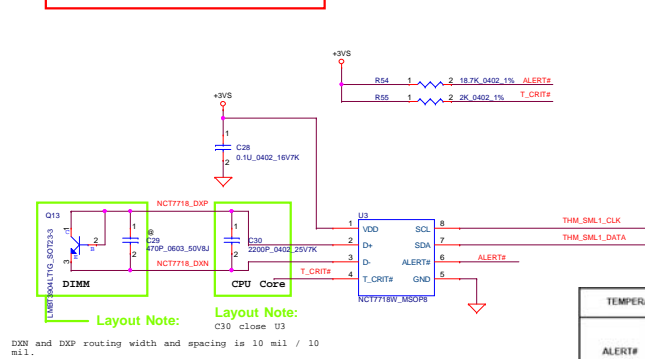
Main Func = FPR



Main Func = OTP

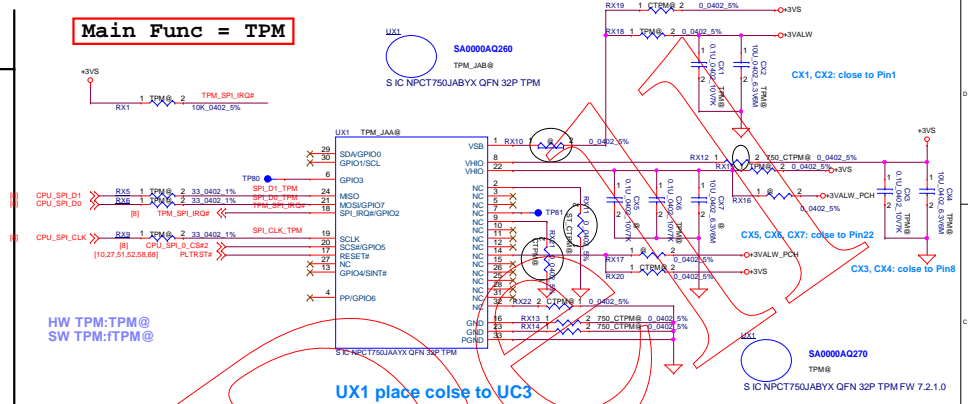


Main Func = Thermal

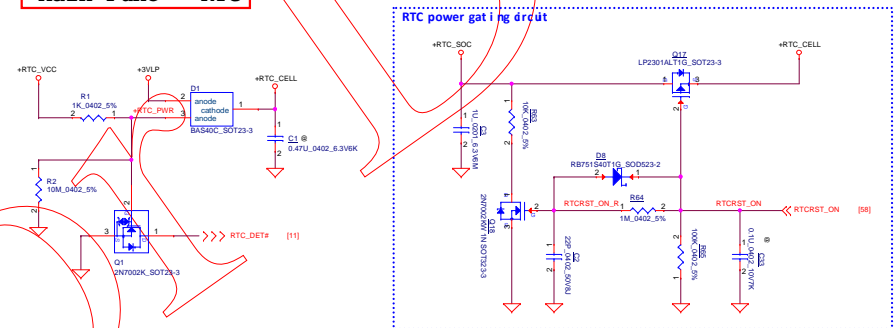



TEMPERATURE (°C)		T. CRIT#				
		2KG	7.5KG	10.5KG	14KG	18.7KG
ALERT#	2KG	77	87	97	107	117
	7.5KG	79	89	99	109	119
	10.5KG	81	91	101	111	121
	14KG	83	93	103	113	123
	18.7KG	85	95	105	115	125

Main Func = TPM

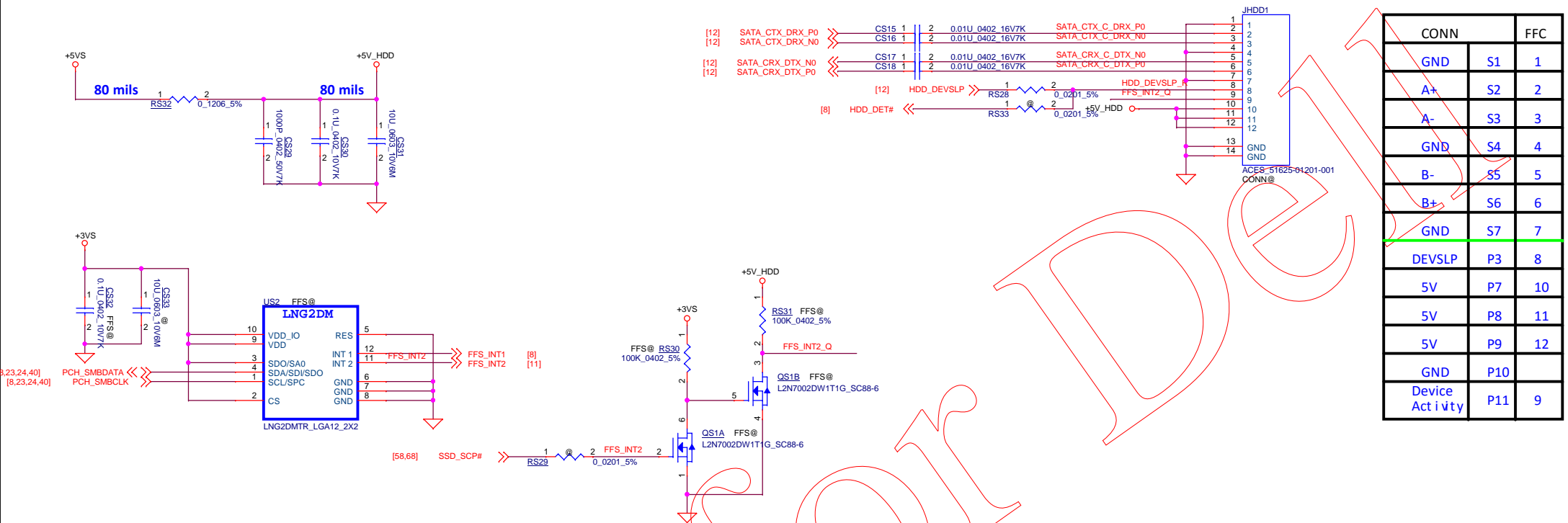


Main Func = RTC

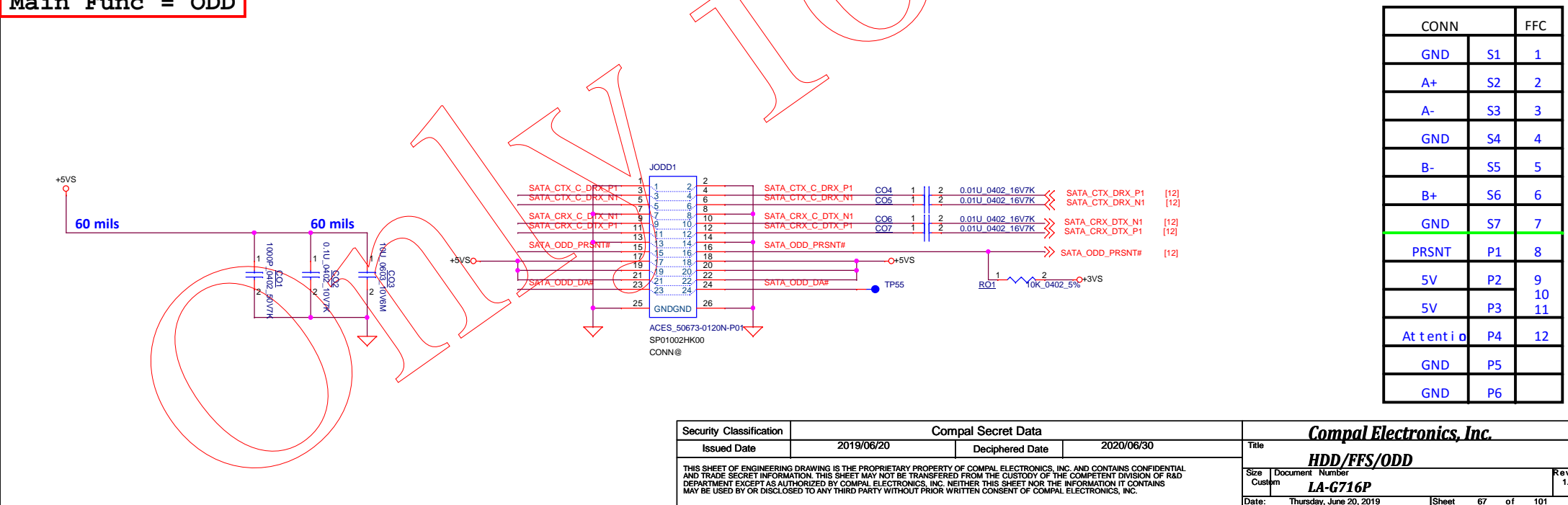


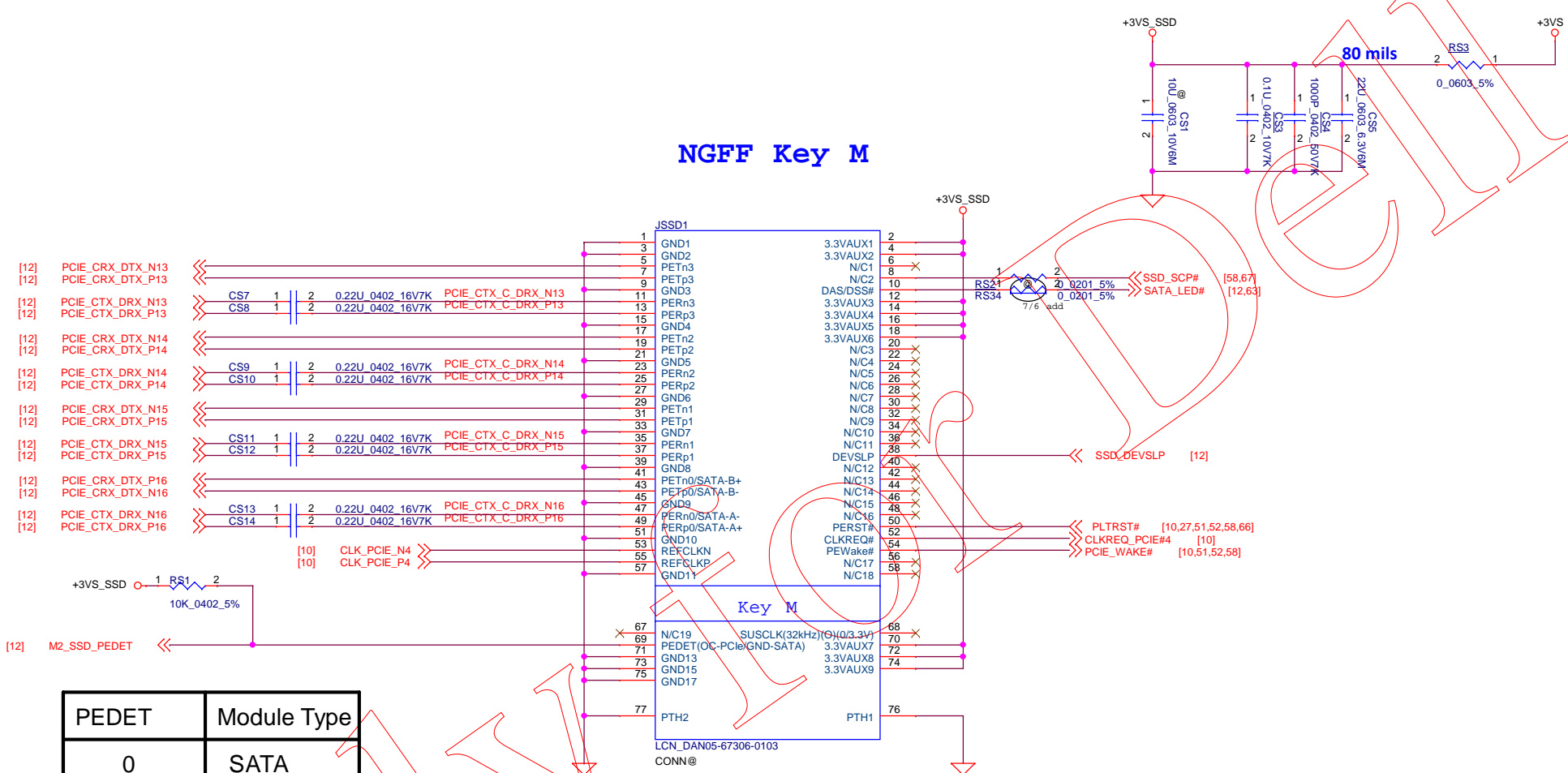
Security Classification	Compul Secret Data			
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# Main Func = HDD&FFS



# Main Func = ODD





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Main Func = eMMC

Only for Del

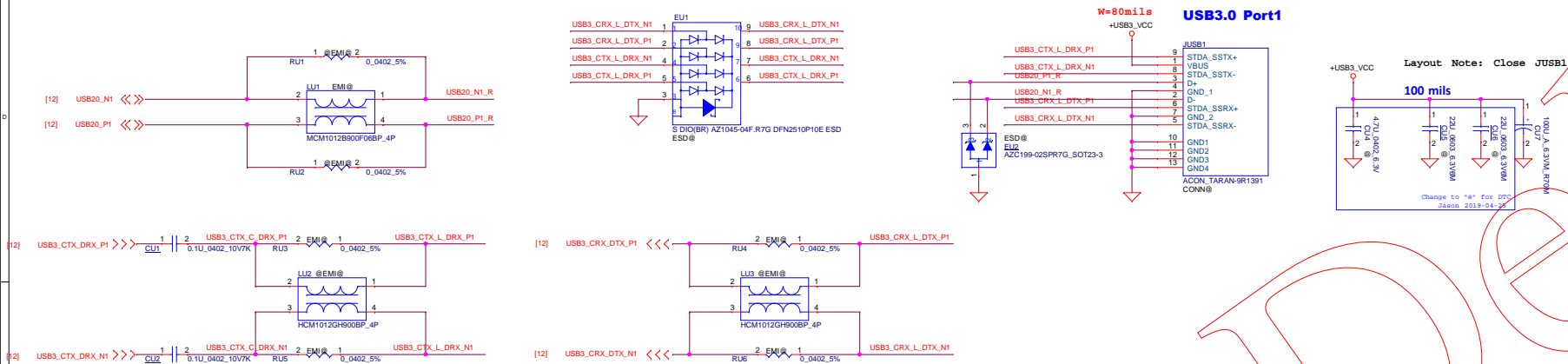
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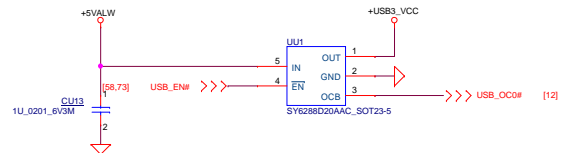
Reserve

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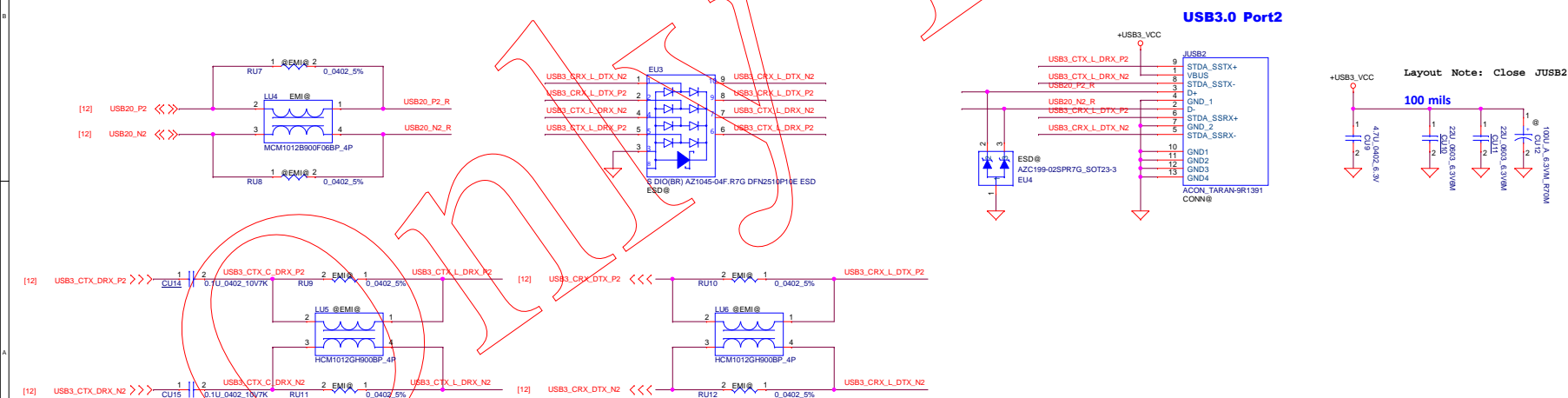
# Main Func = USB3.0 Port1



Maximum Output  
Current 2A



# Main Func = USB3.0 Port2



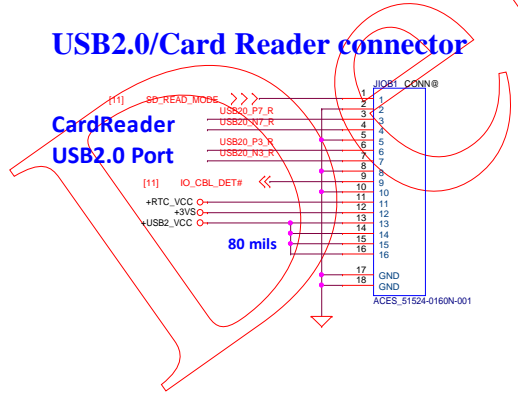
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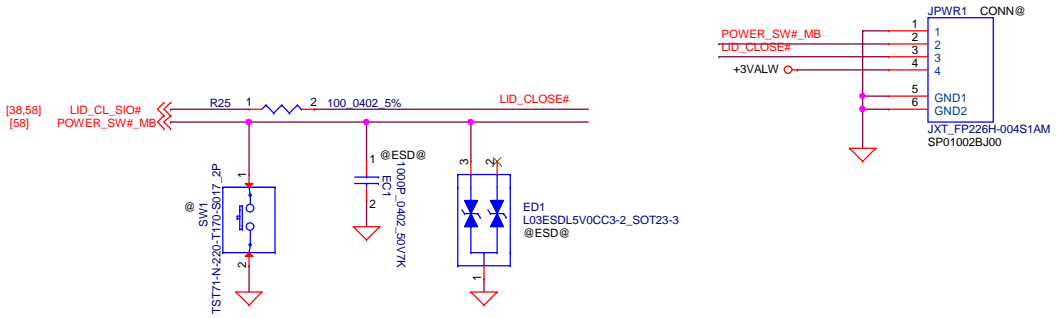
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Main Func = Power BTN

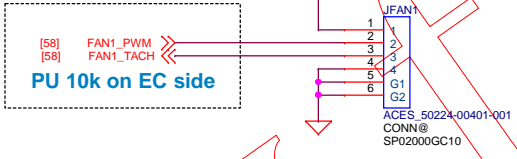
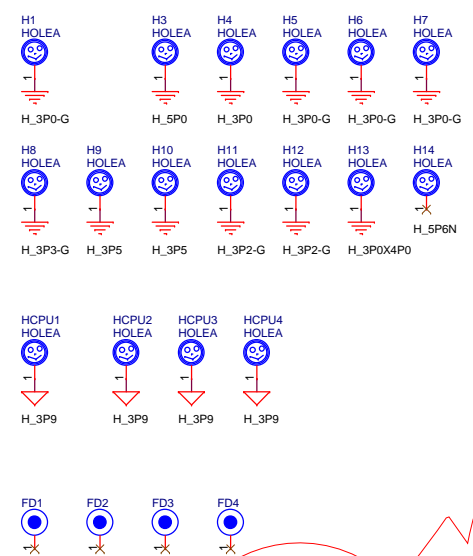
Low actived from KBC GPIO



For EMI Reserved  
LID\_CLOSE# EC2 1 2 0.1U 0402 10V7K

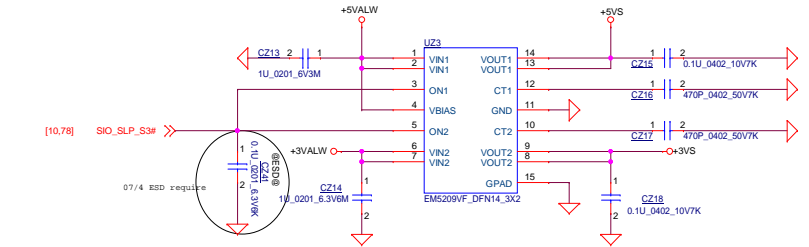
Screw hole/FD

FAN

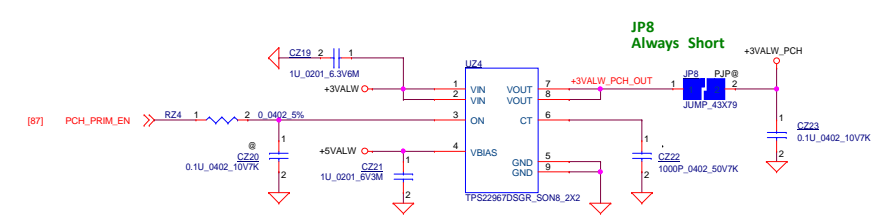


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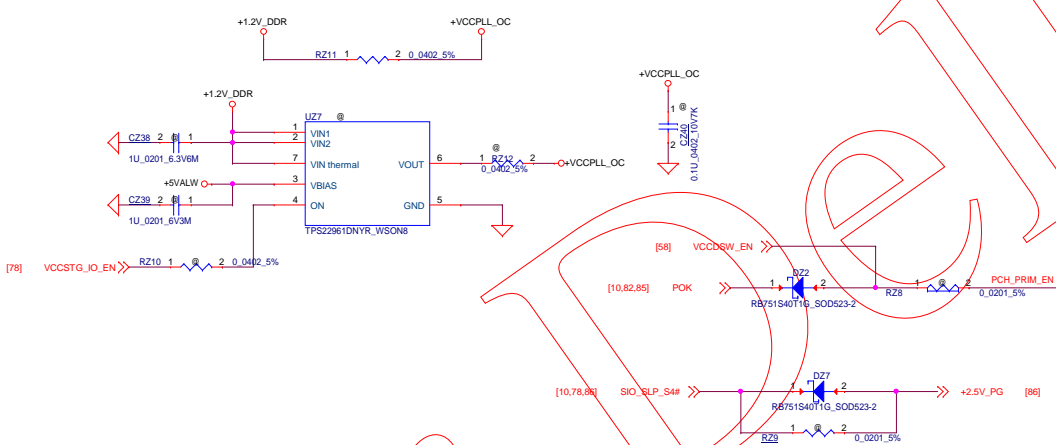
+5VS/+3VS for System



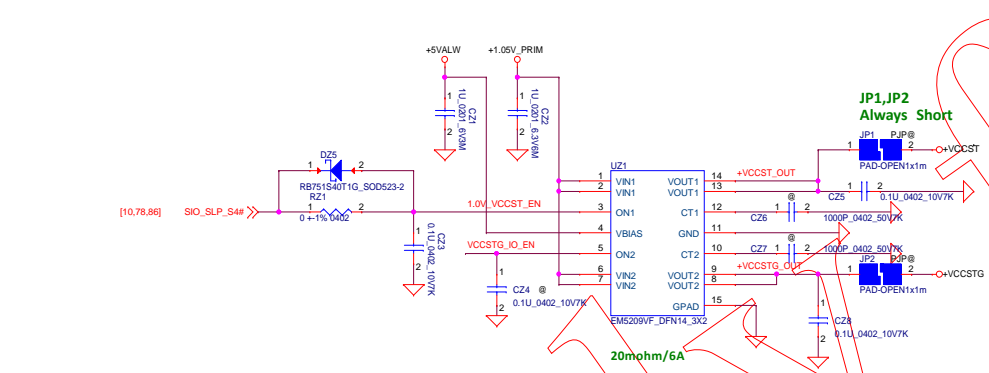
+3VALW\_PCH for System



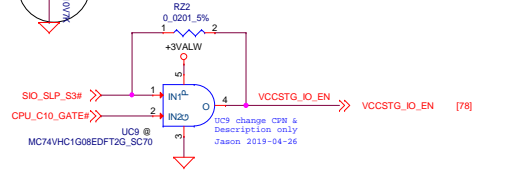
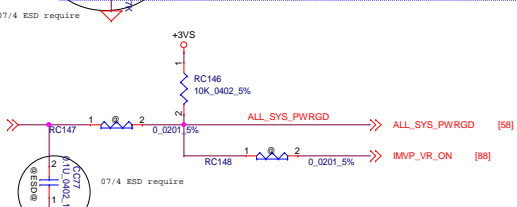
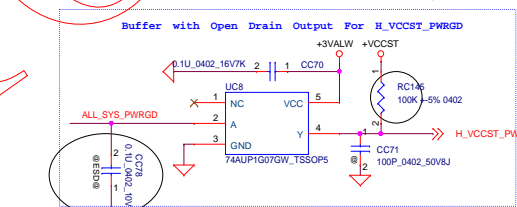
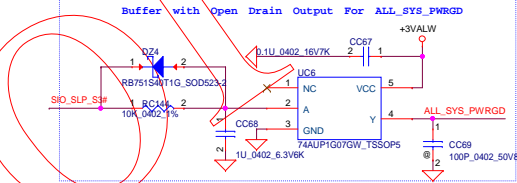
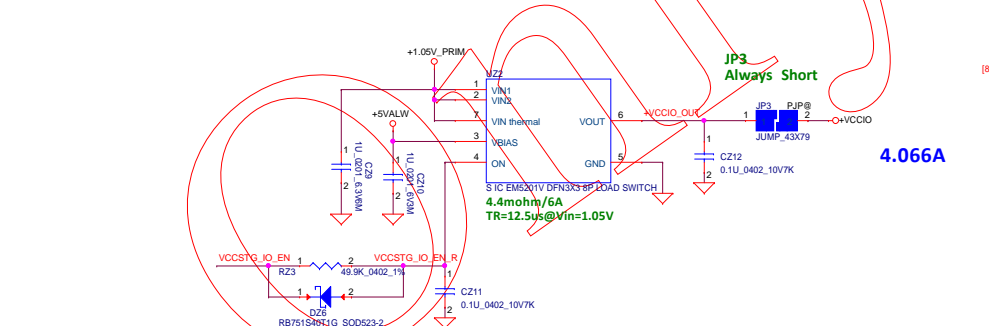
+VCCPLL\_OC source



+1.05V\_PRIM TO +VCCST /+VCCSTG

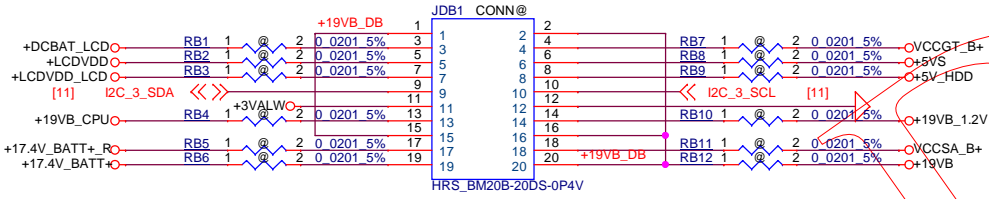


+1.05V\_PRIM TO +VCCIO



Main Func = Power Monitor

RB1 Close to F1  
RB2 Close to R8  
RB3 Close to R8  
RB4 Close to PC123  
RB5 Close to PRB01  
RB6 Close to PRB01



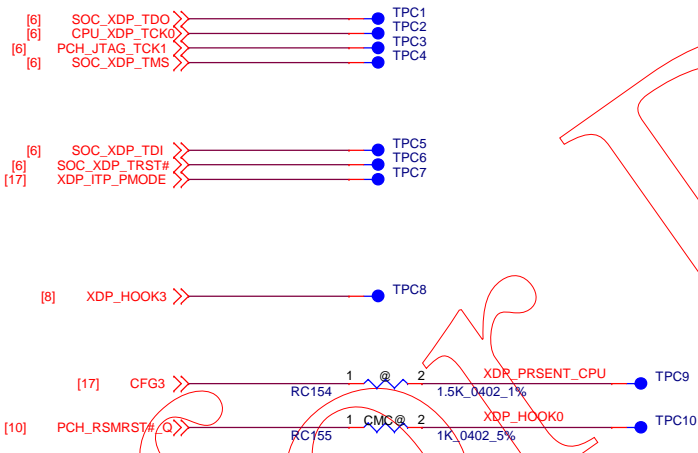
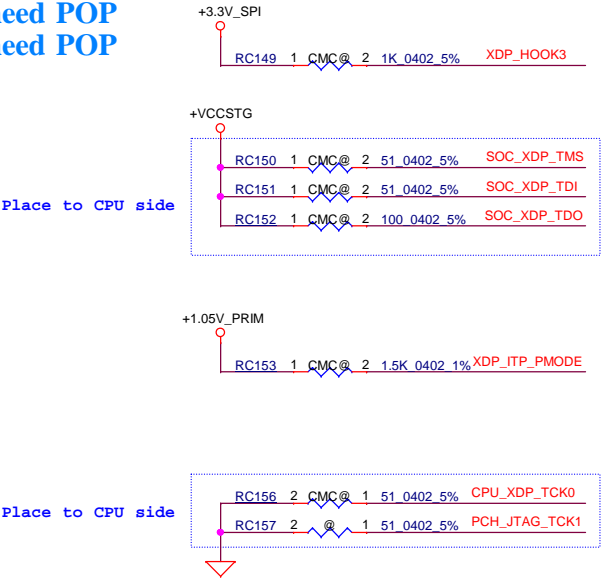
RB7 Close to PIPG01  
RB8 Close to RS32  
RB9 Close to RS32  
RB10 Close to PIPM01  
RB11 Close to PIPA01  
RB12 Close to PIPA01

2	4	6	8	10	12	14	16	18	20
P1+	P1-	P2+	P2-	CLK	GND	P7-	P7+	P8-	P8+
P4+	P4-	P3+	P3-	DATA	3.3V	P6-	P6+	P5-	P5+
1	3	5	7	9	11	13	15	17	19

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PortConnector Less Routing Topology


Follow KBL DCI Link  
RC152 need POP  
RC156 need POP



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Reserve

DELL CONFIDENTIAL/PROPRIETARY

		<b>Compal Electronics, Inc.</b>	
File		Reserve for PWR	
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**SPEC = 6KV**

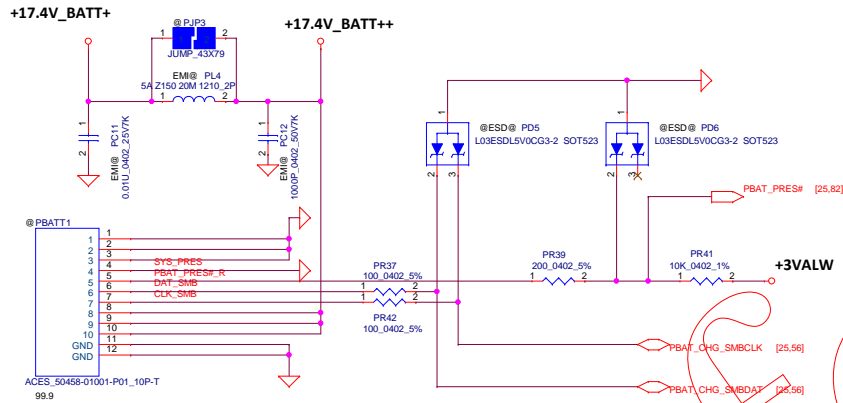


Main Func = BATT\_CONN

Battery Bat Side

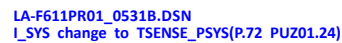
PIN1 GND  
PIN2 GND  
PIN3 GND  
PIN4 SYS\_PRES  
PIN5 BATT\_PRS  
PIN6 DAT\_SMB  
PIN7 CLK\_SMB  
PIN8 Bat t+  
PIN9 Bat t+  
PIN10 Bat t+  
SP021412220

ACES\_50458-01001-P01\_10P-T



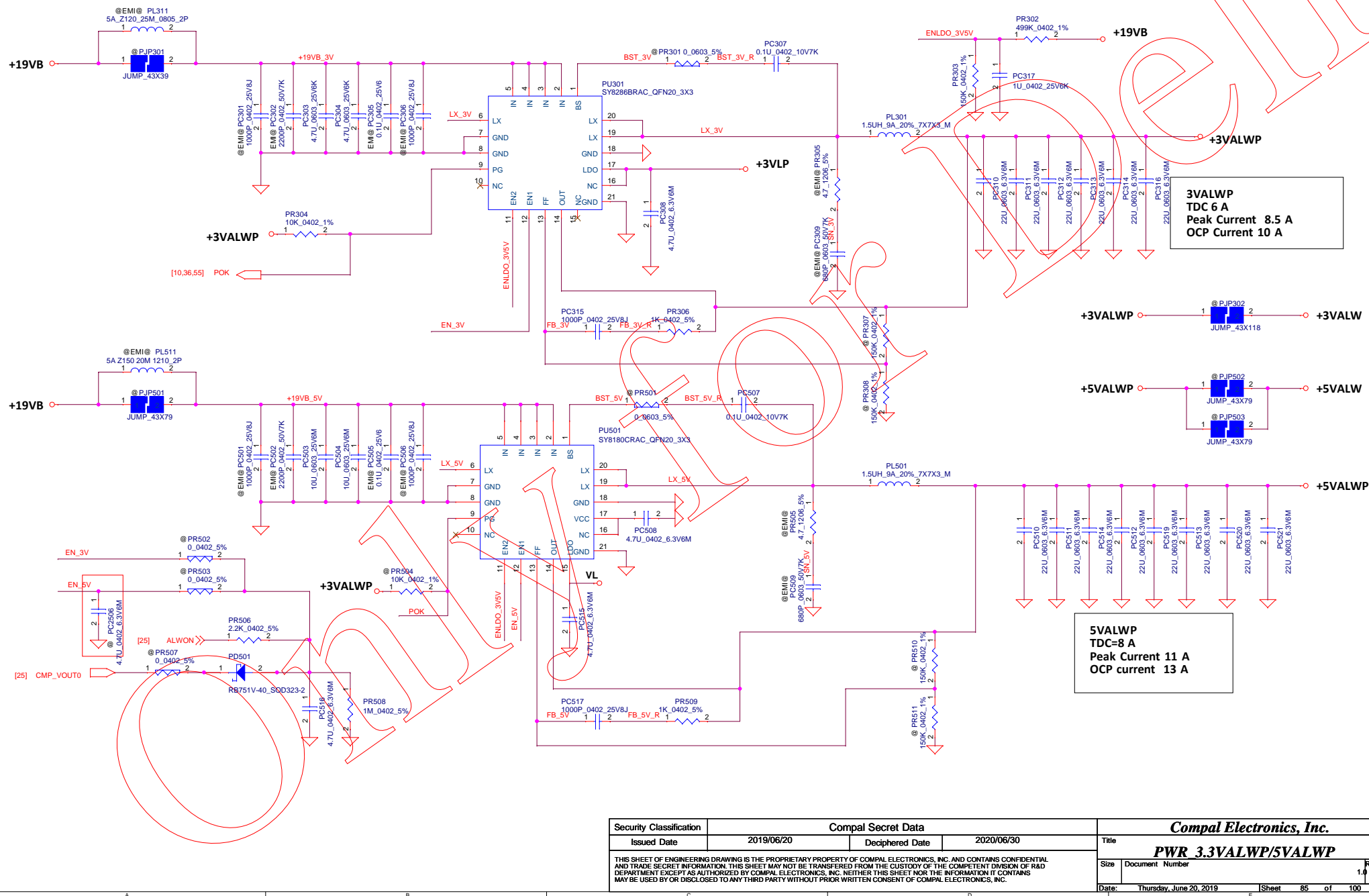
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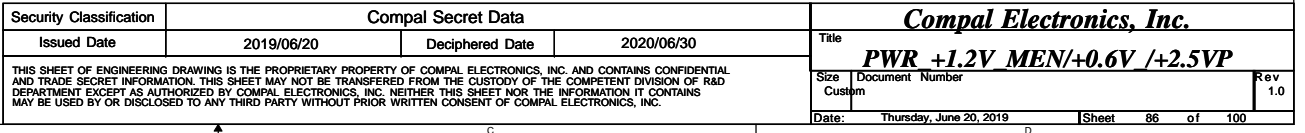
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2019/06/20	Deciphered Date	2020/06/30	Title	
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				Size	Document Number
				LA-F711P	
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Main Func = 3.3VALWP/5VALWP

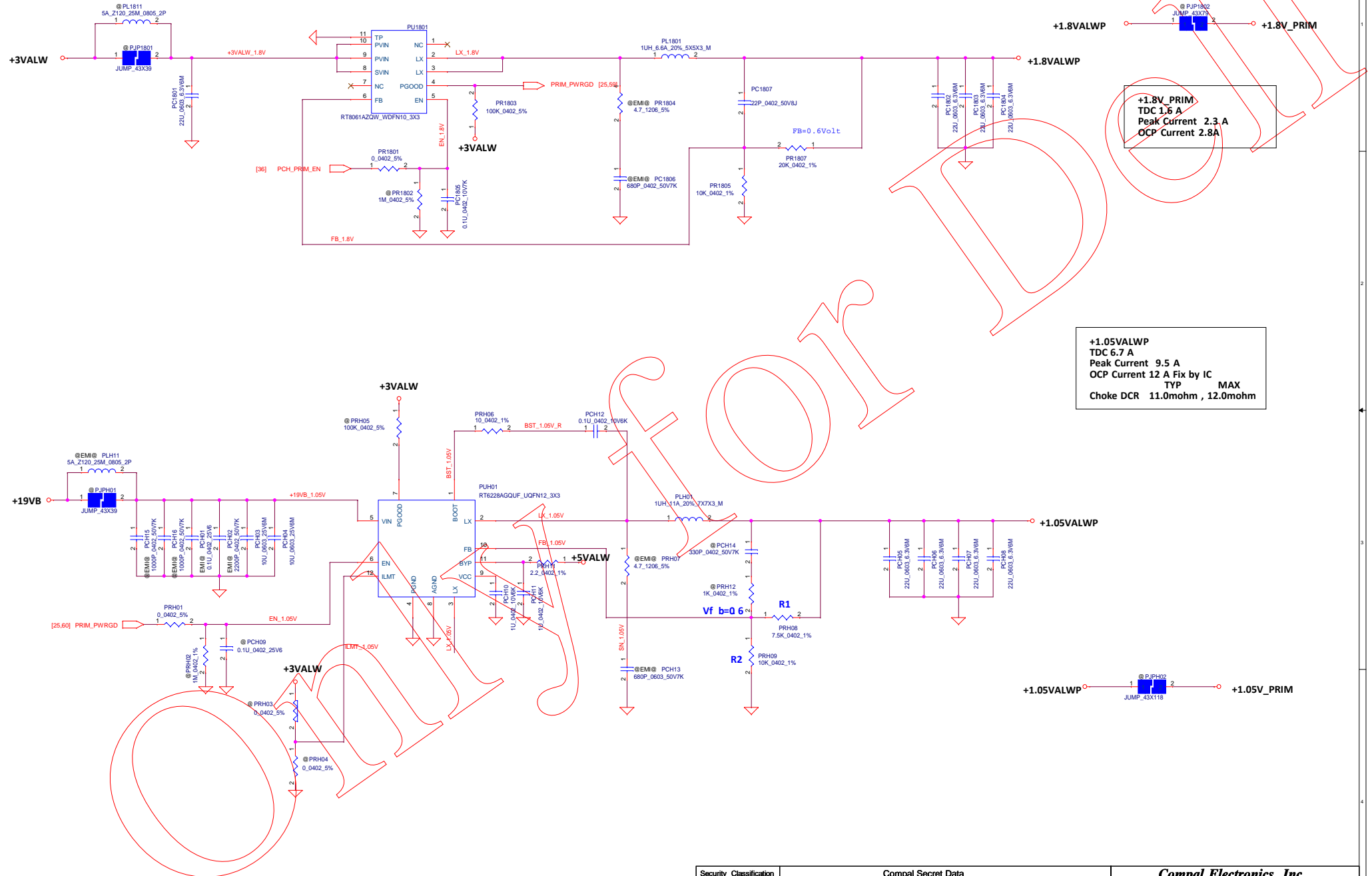


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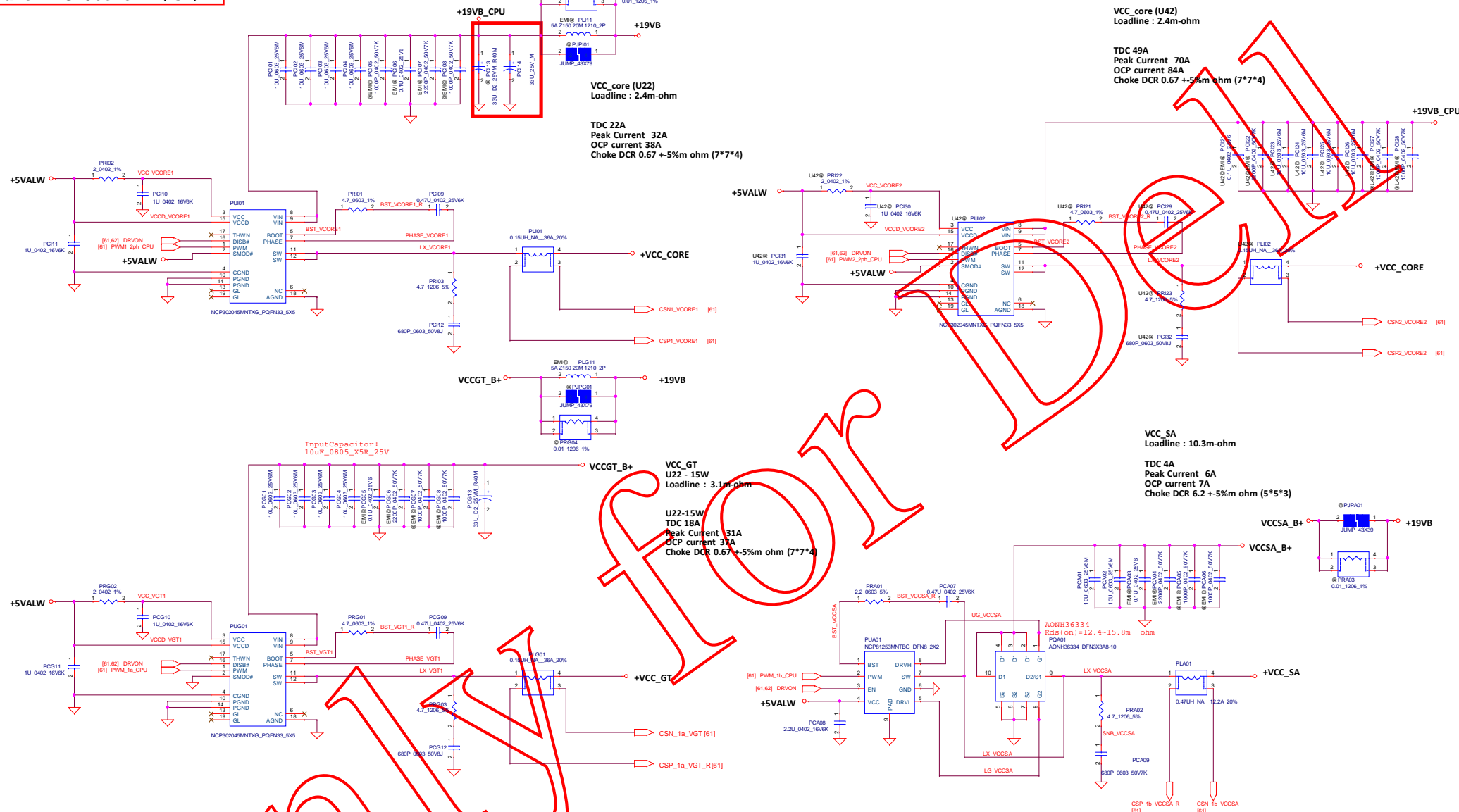
Main Func = +1.8VALWP /+1.05VALWP



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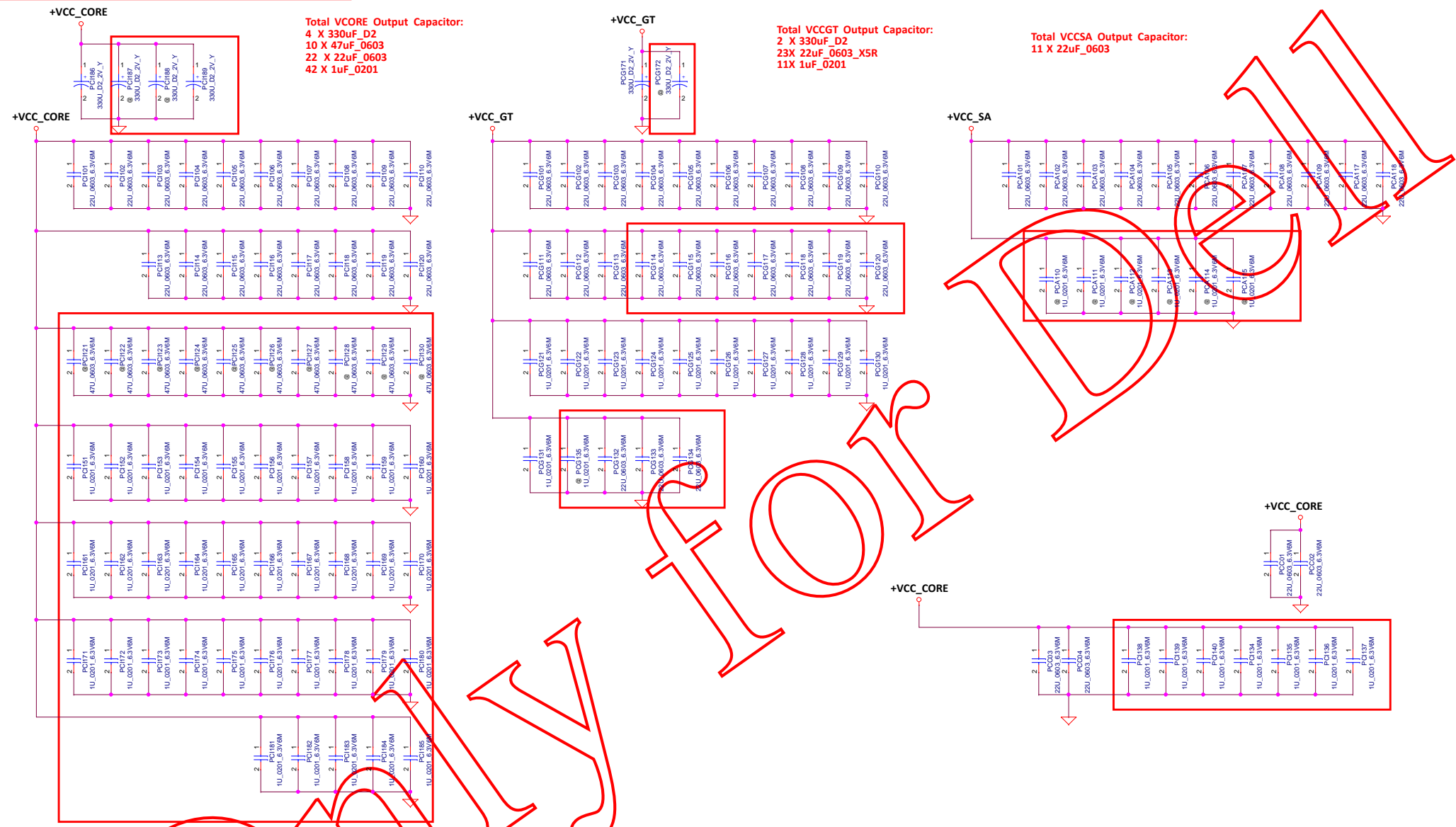
Main Func = CPUcore IA/GT/SA



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Main Func = CPU/ VGA / SA MLCC




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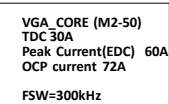
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Issued Date	2019/06/20	Deciphered Date	2020/06/30	Title	PWR_CPU / VGA / SA MLCC
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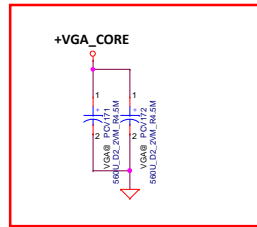
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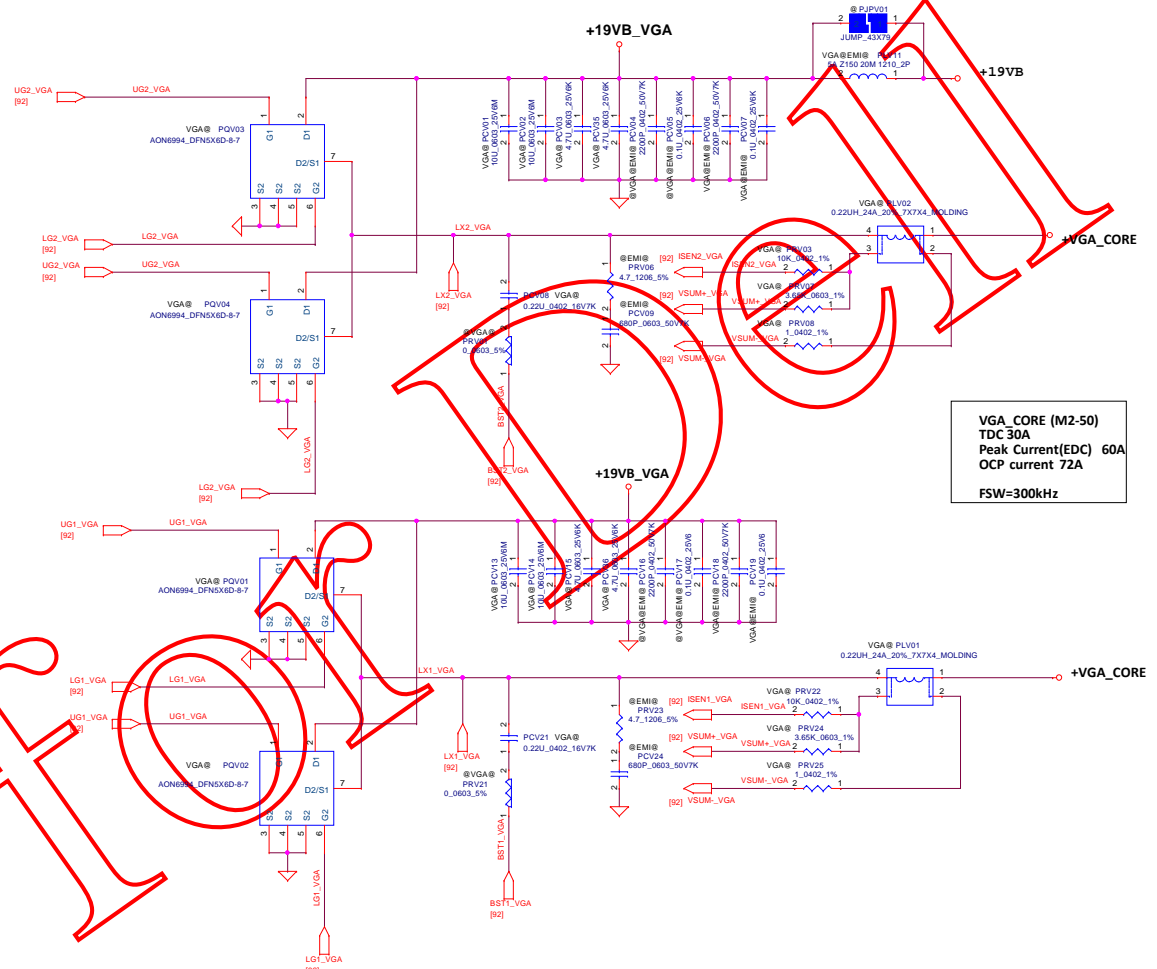


Main Func = VGA CORE



For VGA CORE

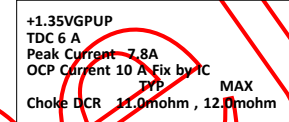
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VGA\_CORE (M2-50)  
TDC 30A  
Peak Current(EDC) 60A  
OCP current 72A  
FSW=300kHz

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Version Change List ( P. I. R. List )

Item	Page#	Date	Request Owner	Issue Description	Solution Description	Rev.
1.	P84-PWR_CHARGER	2018 06/12	Compal	Vendor suggest	Change the PRB11 from 20hm to 10hm	X01
2.	P84-PWR_CHARGER	2018 06/12	Compal	Downsize	Change the PCB06 and PCB18 to 0.47U_0402	X01
3.	P84-PWR_CHARGER	2018 06/12	Compal	Vendor suggest	Change the PCB11 to 0.33U	X01
4.	P89-PWR_CPU_Stage	2018 06/14	Compal	Downsize	PCA07, PCG09, PCI09, PCI29 change to 0.47U_0402	X01
5.	P92-PWR_VGA_CORE(ISL62771)	2018 06/14	Compal	Downsize	Change PCV30 to 0.15U_0402 Change PCV32 to 0.1U_0402	X01
6.	P86-PWR_1.2V_DDR	2018 07/05	Compal	Factory suggest	Change the PDM01 to SCS00000Z00	X01
7.	P83-BATT_CONN	2018 07/05	Compal	ESD request	Change the PD5,PD6 to SCA00002A00	X01
8.	P84-PWR_CHARGER	2018 07/09	Compal	Modify for AC detect	Change the PRB10 from 47K to 52.3K	X01
9.	P90-PWR_CPU MLCC	2018 07/09	Compal	Change for PI simulat i on	move PCI134,PCI135,PCI136,PCI137,PCI138, PCI139,PCI140 to +VCC_CORE_GT	X01
10.	P84-PWR_CHARGER	2018 07/11	Compal	Factory request	Add PCB71, PCB07 change to 1U_0402	X01
11.	P92-PWR_VGA_CORE	2018 07/17	Compal	Downsize	Change the PCV30 to SE000011G00 0.15U 16V	X01
12.	P88-PWR_CPU	2018 07/17	Compal	Change for CPU EA test	Change the PRZ25, PRZ29 to SD014100380 100K Change the PRZ23 to SD034255200 25.5K	X01

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					LA-G716P	1.0
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